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13. ABSTRACT (Maximum 200 words) A single pole evanescent resonator has been designed and built under to test feasibility. The results are shown in Figure 9 and indicate that the filter structure resonates at 3 GHz and has a Q of 460. Due to the lumped element character of the evanescent mode micromachined structure, parasitic resonances do not exist as shown by the measured and theoretically calculated data. As shown by Figure 9, only one resonance from 0 to 20 exists. Higher order periodic resonance are suppressed by the cutoff of the guide and the presence of the post. Using a similar synthesis method, a 2-pole bandpass filter has been designed and simulated at X band. The volume of the designed filter is 10.5 mm ³ compared to a 400 mm ³ 2-pole micromachined resonant cavity filter with comparable performance. To add extra poles to an evanescent filter, additional posts separated by evanescent cavities are needed. This makes multiple pole filters easy to fabricate since only two silicon wafers are needed to create the cavity. The capability to fabricate these filters using IC fabrication techniques [5,6] allows for unique designs that may reduce size even further.			
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FINAL REPORT

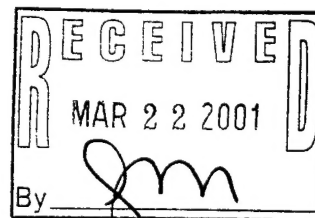
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EVANESCENT MODE FILTERS

By

LINDA P.B. KATEHI

The University of Michigan
Ann Arbor



Hand delivered
by B. Webster...

February 2001

PROJECT DESCRIPTION

A. INTRODUCTION

Typical communication satellites, Figure 1, employ traditional waveguide-base front-end architectures due to excellent electrical performance and high reliability. However, these systems are extremely massive and utilize large volume mostly attributed to the low-insertion loss waveguide switch, diplexer and waveguide-packaged solid-state power amplifier. These three components are interconnected by waveguide sections for compatibility and result in subsystems with dimensions of 18cm x 40.6 cm x 10.5 cm as shown on Figure 2. Despite the large volume

Satellite RF Front End on a Chip



Figure 1: Waveguide-Based Satellite RF Front End

and mass, metallic waveguide has been the transmission medium of choice in space applications due to the low-loss requirements. Its use in communication systems has resulted in overall system loss of less than 2 dBs. Replacement of the waveguide components by micro-machined ones without substantially affecting electrical performance can lead to a breakthrough in wireless communications [1], [2]. Communications via satellites require optimum high frequency performance, lightweight hardware, advanced packaging, high-density interconnect technology and high reliability. The use of electronics in space poses interesting but grand challenges. It also provides the opportunity for using revolutionary concepts in circuit design, fabrication and implementation to achieve what is considered by today's standards as ultimate performance, minimal volume and very low cost. Circuit optimization methods applied to existing technologies cannot meet the specifications, but critical advancements based on new concepts at fundamental levels of circuit design and diagnostics are needed. The capability to integrate the RF system on a single chip while preserving electrical performance will provide a breakthrough in communications systems for any type of wireless applications. The objectives of this effort is to attempt a revolutionary filter/diplexer design by combining on-wafer integration and packaging along with micromachined waveguide-like structures into a single monolithic arrangement that exhibits very high Q and very small size. In addition, this monolithic filter/diplexer will have the ability to electronically achieve reconfigurable performance using MEMS devices. Such a device will provide desired RF filtering in addition to frequency hopping and can be used in filter banks due to very small size. While not as small as microelectrical mechanical filters [4], the proposed filter devices will have a size of the order of a few millimeters for operation in the low end of the microwave spectrum. This size is considerably smaller than that of any other conventional filter

of similar performance. Furthermore these filters can be integrated monolithically with the conventional planar technology while they provide the smallest possible size along with excellent performance for frequency ranges above 1-2 GHz. The filters proposed herein are equivalent to lumped element filters where the lumped L and C is provided by appropriate use of micromachined shapes. This is the underlining concept that leads to drastic reduction of size. To accomplish this goal advanced electronics material technology and high-density fabrication techniques along with electromagnetic analysis tools will be used. The development of such a diplexer will demonstrate a key concept in advanced communications architectures and systems and it will have a major impact on wireless technologies.

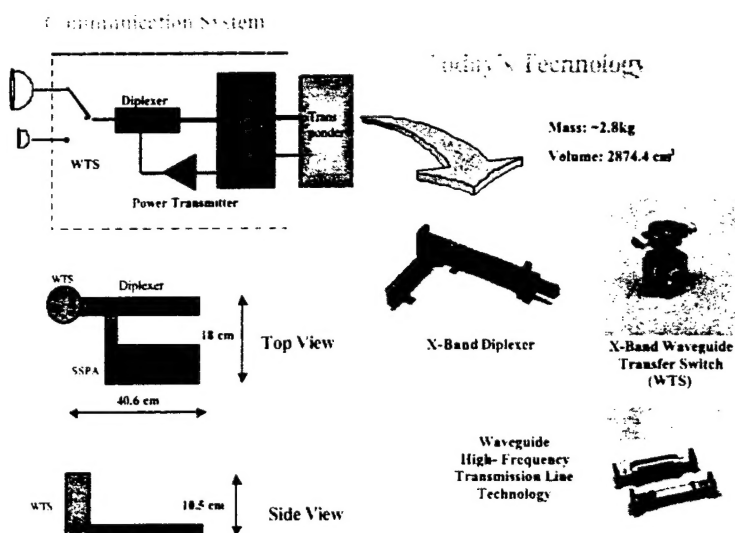


Figure 2: Conventional Waveguide RF Front-End

B. SUMMARY OF PERFORMED STUDY

The design of a diplexer reduces to a filter design with two very narrow communication frequency channels highly isolated. The study performed during the past few years at Michigan has demonstrated the following [1-6]:

- In monolithic filter designs high-Q performance can only be achieved by use of filters that include either resonating elements printed on membranes or micro-machined waveguide resonant cavities (Figure 3).
- The use of membrane filters is limited to high frequencies due

- to the size of the resulting filter and because the achieved Q cannot exceed 500 [1,2].
- Si micromachined resonator cavity filters can be integrated monolithically and can provide filters with drastically reduced height (1-1.5mm). However, the use of resonating cavities limits the transverse size of the filter to a minimum of half free-space wavelengths (about 1.5mm in W band, 1.5cm in X band and 5 cm in L Band) [3].
- The need to acquire high Q and small size for frequencies in the low end of the microwave spectrum, requires the use of non-resonant monolithically integrated structures.
- Small size and high Q may require frequency and input match adjustments. Such adjustments, while necessary in the case of very high-Q filters, are impossible without the use of MEMS.
- The ability to electronically adjust frequency and bandwidth will provide additional filter functionality such as tunability, switching and frequency hopping that will lead to new architectures in communication systems.

Based on the above observations we concluded that small size, high-Q filters or filter banks with high isolation between the adjacent channels can be achieved by use of micromachined evanescent-mode cavities that appropriately couple to each other and to the input/output microstrip or coplanar waveguide transmission lines. The use of evanescent mode structures provides small size and the ability to tune via small variations in some geometrical parameters using MEMS structures. Studies performed at Michigan under ARO/DARPA funding have demonstrated the potential of this filter technology to provide excellent performance and very small size. While, high isolation between the adjacent channels is intrinsic to this filter design,

further improvements can be achieved by use of the on-wafer packaging techniques, which have demonstrated the ability to provide isolation as high as -80dB to -100 dB [4-6].

The performed study focused on the development of evanescent mode resonators and filters using micromachined cavities in S, L and X Bands. A detailed description of the effort in each one of these two steps is given below.

Evanescent-mode Micromachined Filters

Historically, the development of microwave passive networks consists mainly of finding distributed components which can provide inductive and capacitive reactances to replace the lumped elements employed by the filter prototype. Although distributed constant networks are necessary for high-Q reactance elements, it is not essential that these networks be based on structures of the conventional transmission line type. For some years now literature has been accumulating on network designs using waveguide sections below their cut-off frequency [17,18]. Lebedev and Gutsait [18] first discovered that resonant behavior can be achieved by any evanescent mode if appropriate terminating conditions (a conjugate reactance) for that mode are provided.

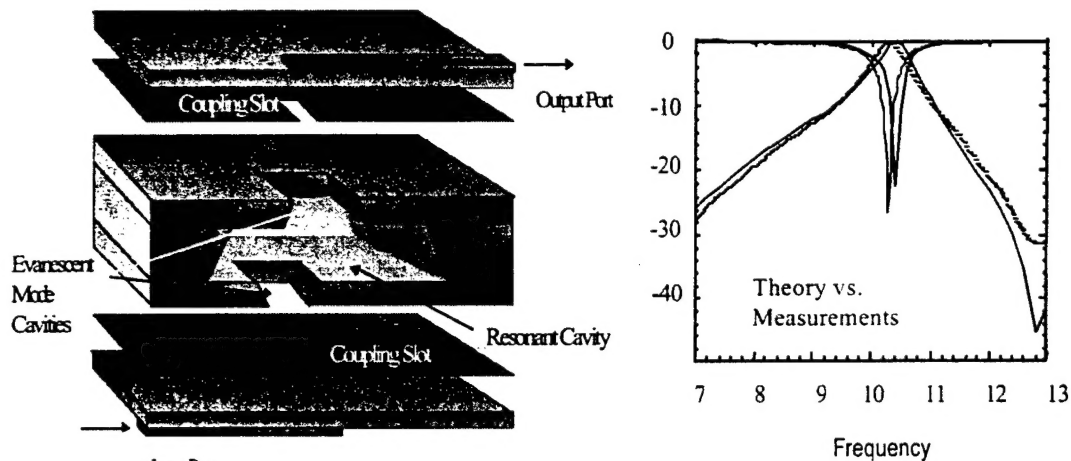
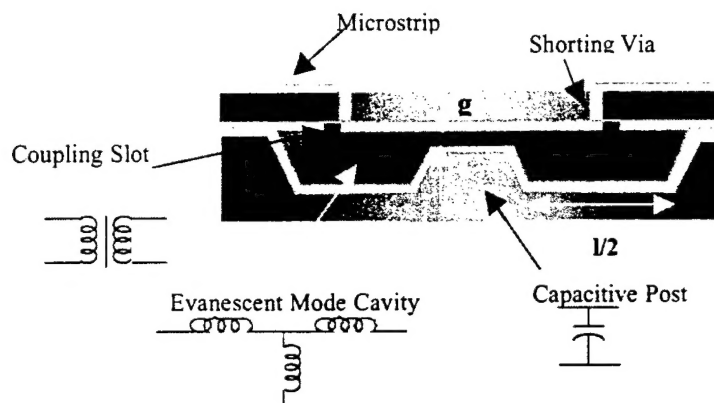
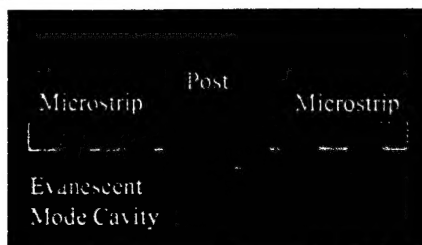


Figure 4: Single-Pole Micromachined Filter with Evanescent and Resonant Cavities. Theoretically Calculated $Q=569$, Measured $Q=530$. Insertion Loss at Center Frequency $IL=-3dB$, $BW=5\%$.

Any band-pass filter can be realized from a low-pass prototype by appropriate frequency and impedance scaling. The band-pass prototype can then be converted to a lumped element configuration where shunt LC resonators are connected through admittance inverters. It can be proven that this circuit can reduce into a sequence of evanescent waveguide sections appropriately connected by negative (capacitive) coupling. This negative coupling can be provided by resonating cavities as shown in Figure 4 [17] or by capacitive obstacles as shown in Figure 5. Preliminary results have demonstrated the ability to provide very high Q using resonating cavities (see Figure 6). While this approach results in filters that compare to waveguide ones in terms of performance requirements, the size of the resonating cavities may prohibit operation at lower frequencies. Specifically for a 5%, X-band single pole filter, the size of the resonating cavity is 15mm (width) x 20mm (length) x 0.5mm (height). The same filter scaled to 2.5 GHz would require a length of 8 cm and a width of 6 cm.



Magnetic Field Patterns of an Evanescent Mode



H-Field Distribution in the Evanescent Mode Sect

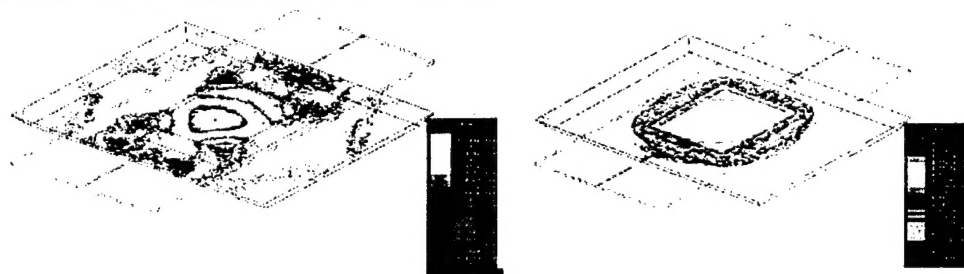


Figure 6: Single Pole Evanescent Mode Filter

When size becomes an issue, the evanescent mode filter of Figure 5 is an appropriate solution where by use of capacitive obstacles such as the pyramidal posts, shown in the figure, required filter size is reduced considerably. As part of this effort we will demonstrate the use of this approach in the development of S- and X- band tunable filters and we will evaluate the effectiveness of the design in terms of size, Q, bandwidth and loss.

The lumped element equivalent to the inductive section (evanescent mode section) of guide is that of a transmission line with imaginary impedance and leads to the development of effective lumped element models. For a single pole evanescent mode filter the lumped element models are shown in Figures 6 and 7. Empirical formulas for the capacitance and inductance of the variable height post have been developed using quasi-static techniques and have been verified by a high-frequency simulator, Ansoft's HFSS. These formulas can be utilized to design an evanescent

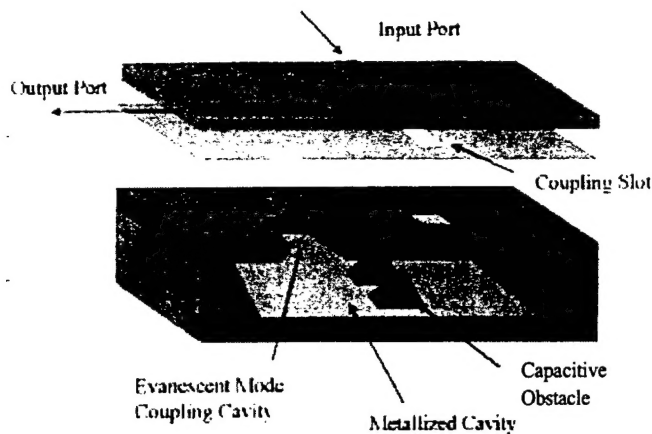


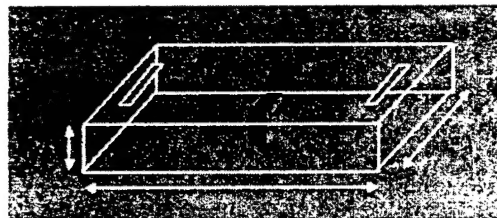
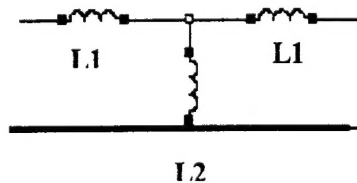
Figure 5: Evanescent Mode Filter with Capacitive Posts

feasibility. The results are shown in Figure 9 and indicate that the filter structure resonates at 3 GHz and has a Q of 460. Due to the lumped element character of the evanescent mode micromachined structure, parasitic resonances do not exist as shown by the measured and theoretically calculated data. As shown by Figure 9, only one resonance from 0 to 20 exists. Higher order periodic resonance are suppressed by the cutoff of the guide and the presence of the post. Using a similar synthesis method, a 2-pole bandpass filter has been designed and simulated at X band. The volume of the designed the filter is 10.5 mm³ compared to a 400 mm³ 2-pole micromachined resonant cavity filter with comparable performance. To add extra poles to an evanescent filter, additional posts separated by evanescent cavities are need. This makes multiple pole filters easy to fabricate since only two silicon wafers are needed to create the cavity. The capability to fabricate these filters using IC fabrication techniques [5,6] allows for unique designs

multi-pole filter from the standard low-pass designs. In addition, from these simple formulas trends may be developed to predict changes in filter response, center frequency and bandwidth, by inducing small changes in dimensions, as shown in Figure 8. Understanding these trends is very important to electronic tuning via use of MEMS devices

A single pole evanescent resonator has been designed and built under to test

Model of an Evanescent Section and of Capacitive Post



$$L_2 = \frac{Z_{cavity}}{w \cdot \sinh\left[\gamma_s \cdot \frac{l}{2}\right]}$$

ω = Radial Frequency
 λ = Free Space Wavelength
 λ_c = Cutoff Wavelength of The Guide
 k = Wavenumber
 τ = Free Space Impedance (120π)
 K = Proportionality Constant

$$L_1 = \frac{Z_{cavity} \cdot \tanh\left[\gamma_s \cdot \frac{l}{2}\right]}{w}$$

$$Z_{cavity} = \eta \cdot \frac{b}{a} \cdot \frac{1}{\sqrt{\left(\frac{\lambda}{\lambda_c}\right)^2 - 1}}$$

$$C_{post} = \frac{K \cdot \epsilon' \cdot (W_x)^{2.5} \cdot (W_y)^{2.5}}{(1 - g)}$$

$$\gamma_s = k \cdot \sqrt{\left(\frac{\lambda}{\lambda_c}\right)^2 - 1}$$

Figure 7: Empirical Formulas for Evanescent Mode Sections where W_a and W_x is the Width and Length of the Capacitive Post

that may reduce size even further.

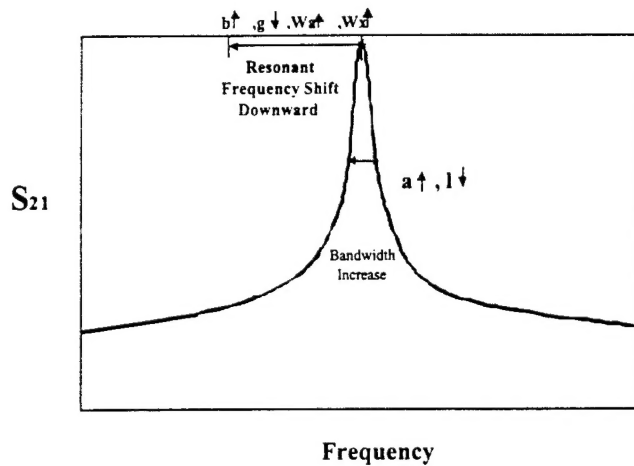


Figure 8: Trends in Filter Performance Based on Varying Geometrical Parameters

Figure 10 indicates two possible designs for a multipole evanescent mode filter. Specifically, Figure 10a shows a three-pole evanescent mode filter with the input and output lines in a perpendicular orientation to further reduce size. The second design (Figure 10b) illustrates a possible arrangement for a diplexer with high isolation between the two output ports. This diplexer is made out of two cavities stacked vertically and coupled through a slot that has a gamma shape designed to control the coupling. This configuration is expected to have very high isolation between the two output ports

due to the high out-of-band isolation in each filter and the relative position between the two cavities.

Fabricated 3 GHz, 20 by 17 by 2mm, Resonator Rejection of Periodic Resonance

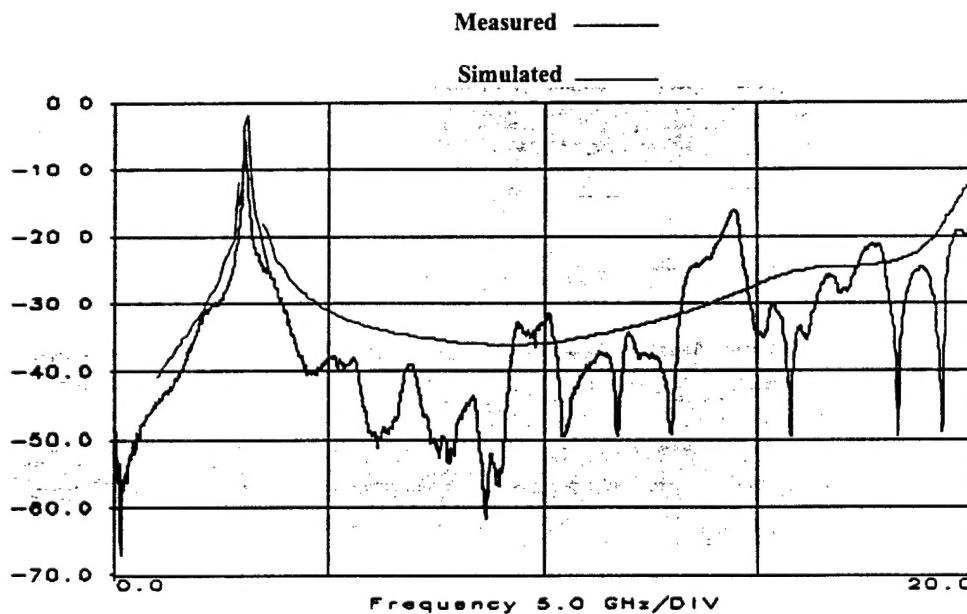


Figure 9: Measurements vs. Theory for a Single Pole Filter

While some very fast initial successes have been achieved, the full potential of the approach has not been explored. This further study is presently underway (funded by NSF) to understand the impact of tuning on the evanescent modes and their implication to filter design. Furthermore, in addition to Si, the evanescent cavities are developed using a variety of materials including electromagnetic bandgaps.

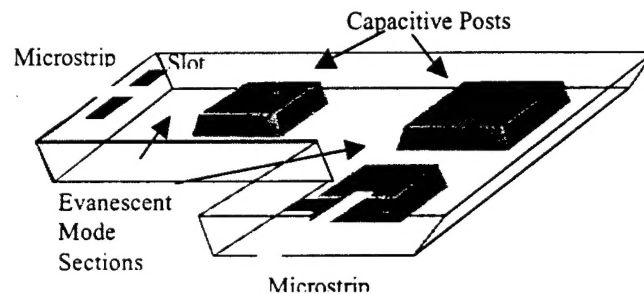


Figure 10a: Three-Pole Evanescent Mode Filter

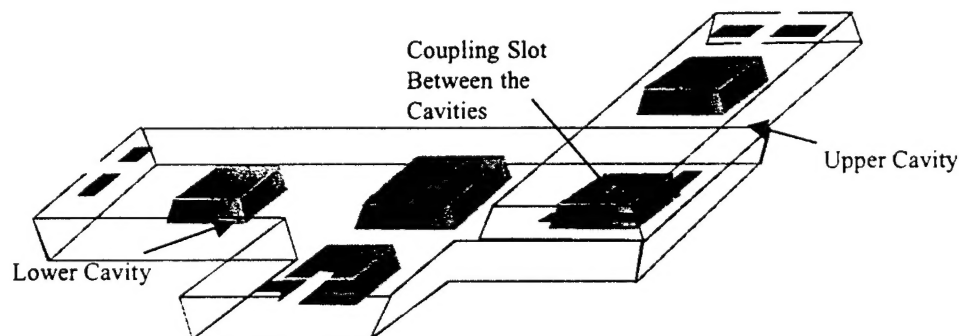


Figure 10b: Diplexer Design with Multiple Cavities Including Evanescent Mode Sections and Capacitive Posts.

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University of Michigan
College of Engineering
Department Chairs Meeting

Tuesday, February 20, 2001
3:15-5:00 PM
2210 LEC

Agenda

1. FY2001 Mid-Year Financial Reports for COE Academic Departments (Mary Kurta)
2. Preliminary Financial Planning for FY2002 and Schedule for Budget/Merit Processes (Judith Pitney)
3. Supplemental Funds for Instruction (Linda Katehi)
4. Status Update of Promotion/Tenure Process (Linda Katehi)
5. Environment – Update on MIT Conference (Linda Katehi)
6. Excellence in Staff Service Awards Nominations (Steve Director)
7. Nominations for National Awards (Steve Director)

University of Michigan



Microfilm Reproductions

Theresa A. Schmitt, Linda P.B. Kahl
University of Michigan Radiation Lab

Theresa A. Schmitt, Linda P.B. Kahl



University of



Graduate

- ✱ Investigate Possible Architectures
- ✱ Identify Trade-Offs
- ✱ Develop Designs
- ✱ Fabricate and Measure to Verify



Figure 1. Micrograph of the device.

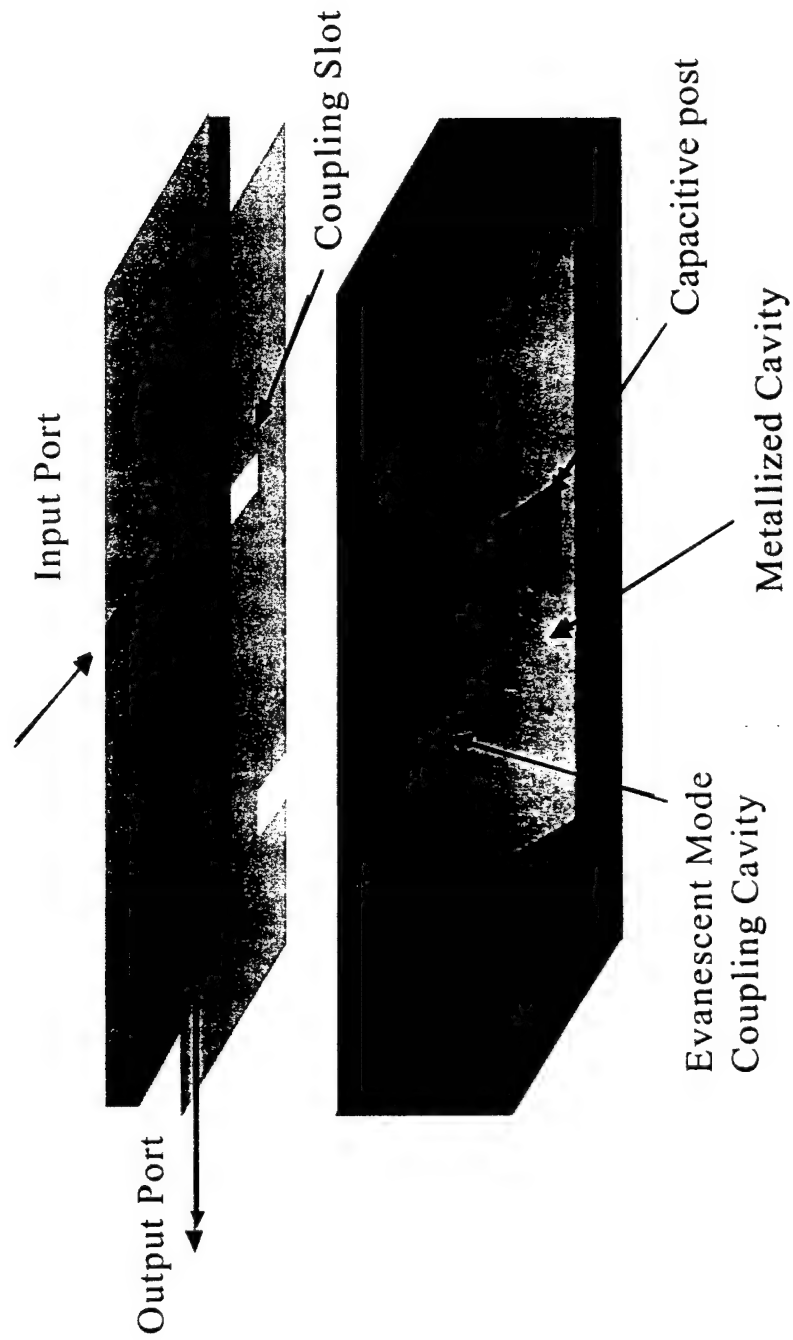
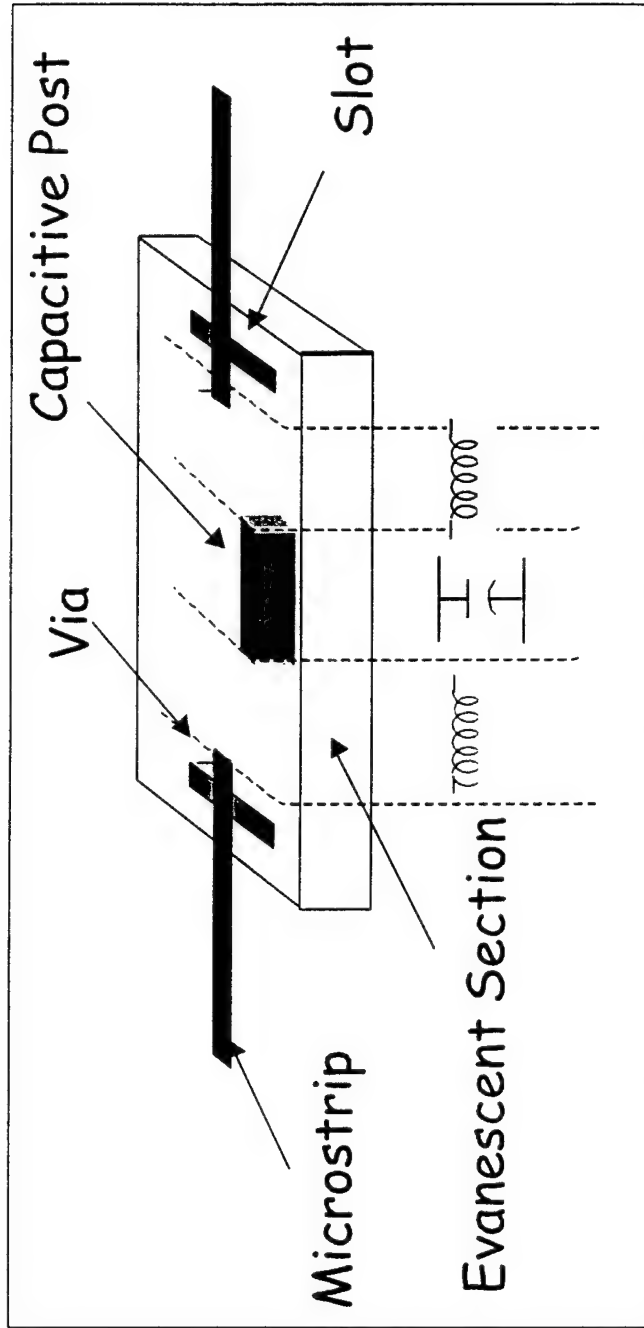




Figure 1. Microstrip antenna with capacitive post, slot, and via.





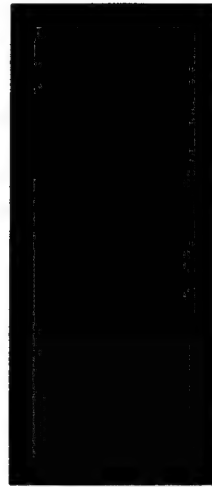
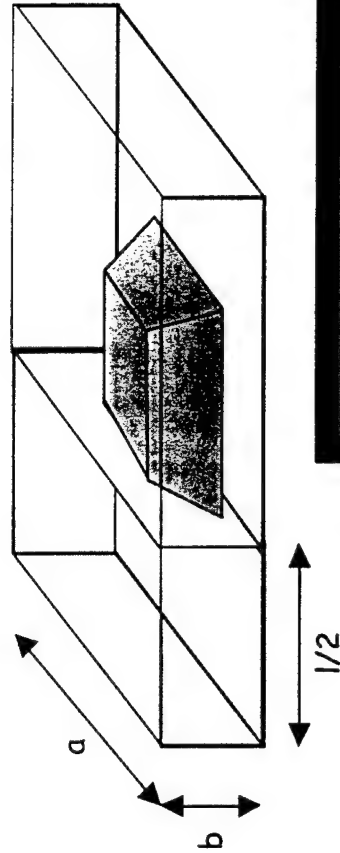
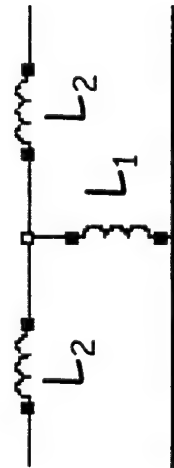
University of



Department of

Electrical Engineering

Ann Arbor, Michigan

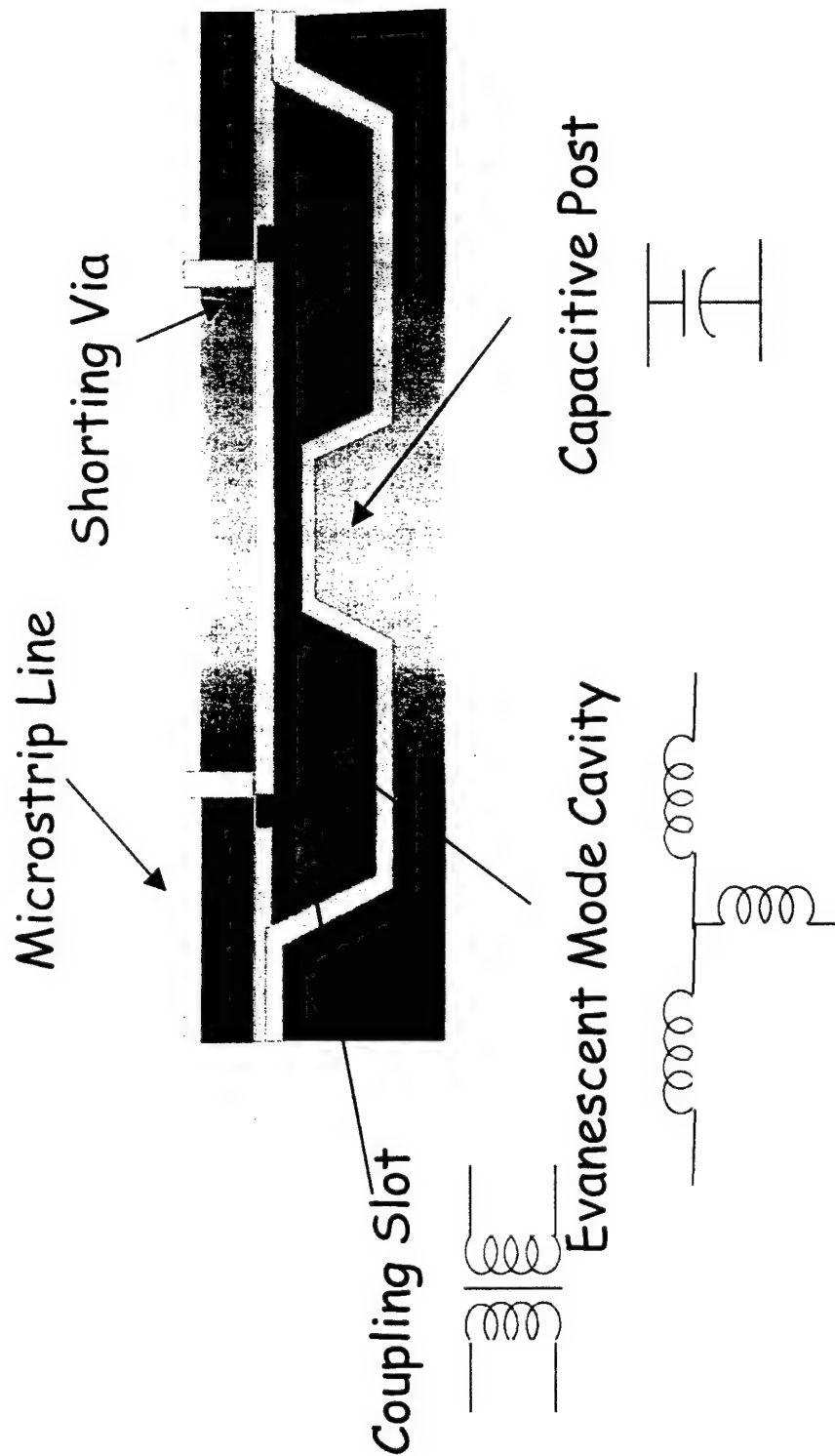


w = Radial Frequency
 λ = Free Space Wavelength
 λ_c = Cutoff Wavelength of The Guide
 k = Wavenumber
 η = Free Space Impedance (120π)



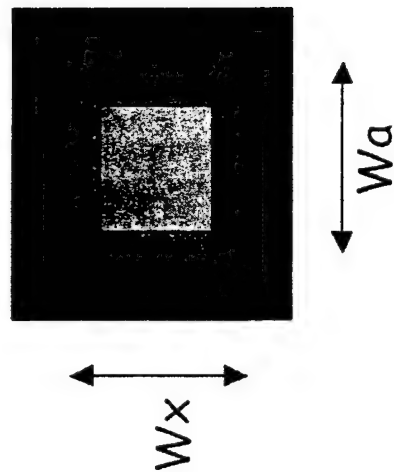
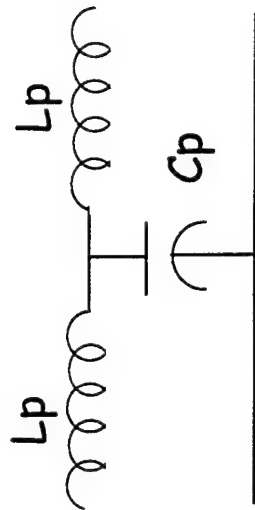
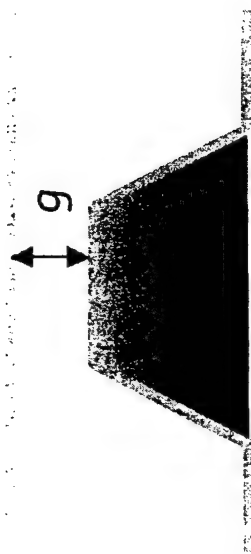


Figure 1. Microstrip Line



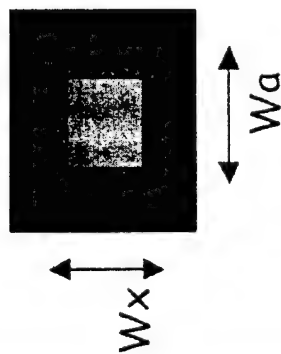
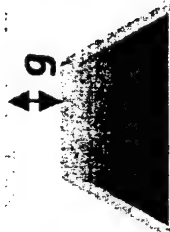


Modeling



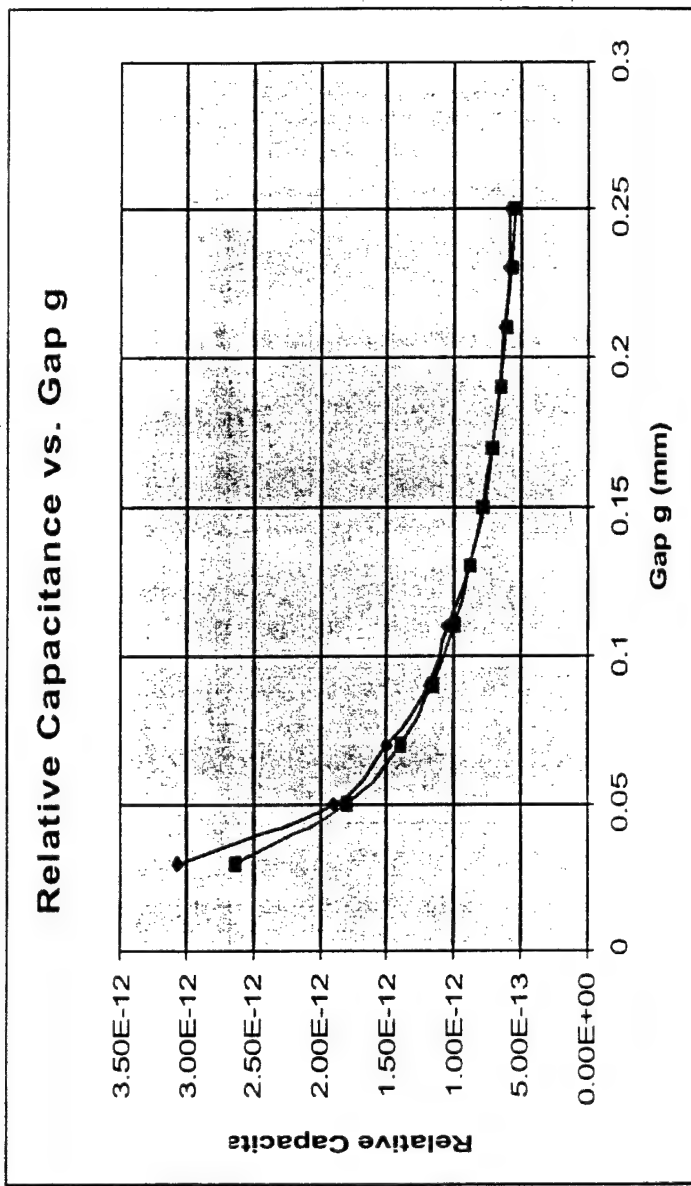
$$C_{post} = \frac{K^* \epsilon^0 * [(W_a)^{0.63} * (W_x)^{0.33}]}{(1-g)}$$

$$L_p = A^* [W_a * W_x * (1-g)] + B$$



— Quasi-Static

— Full-Wave



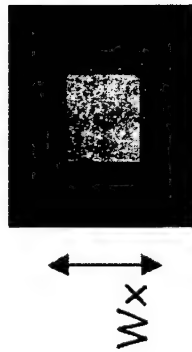
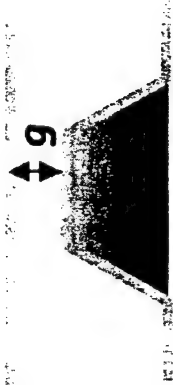


University of Michigan



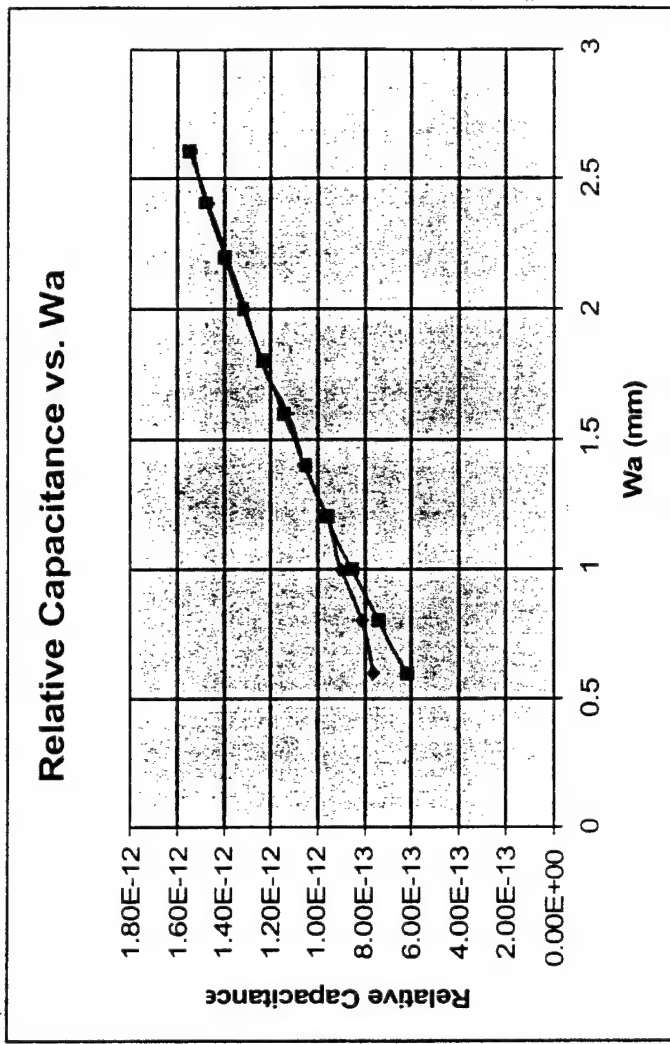
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— Quasi-Static

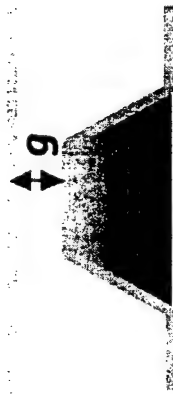
— Full-Wave





University of
Michigan

Power Capacitors



W_x

W_a

Quasi-Static

Full-Wave

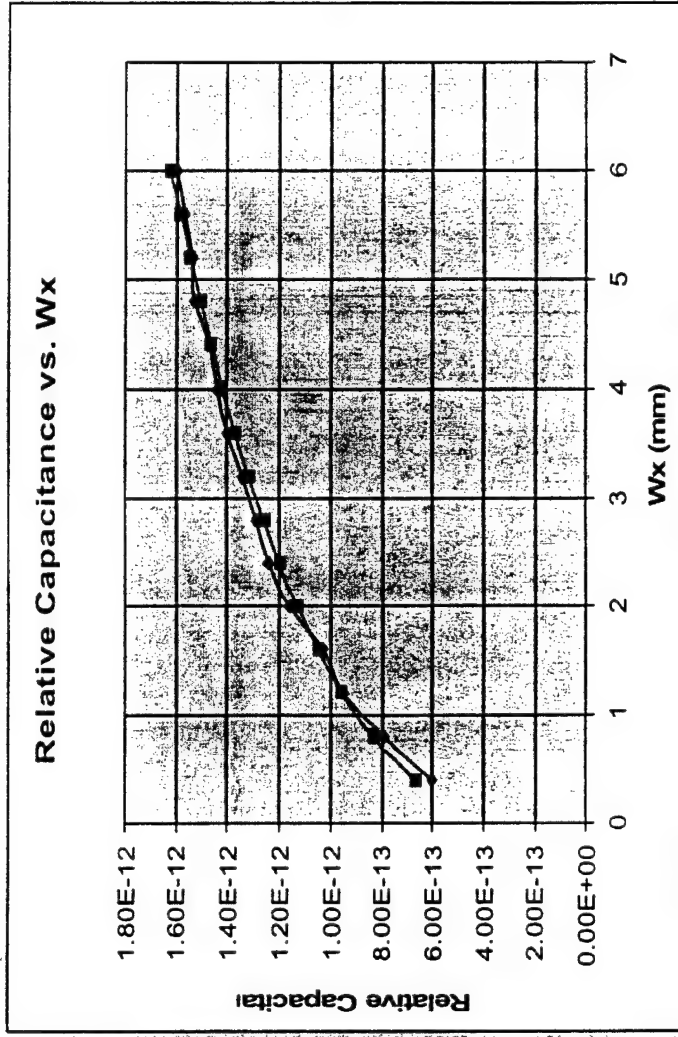
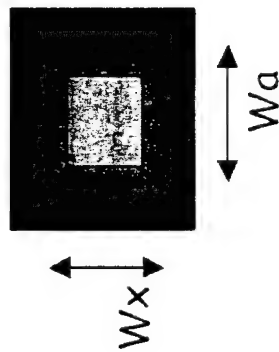
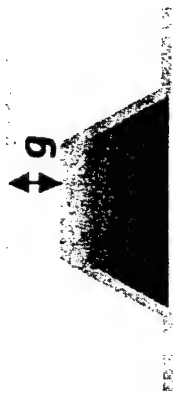


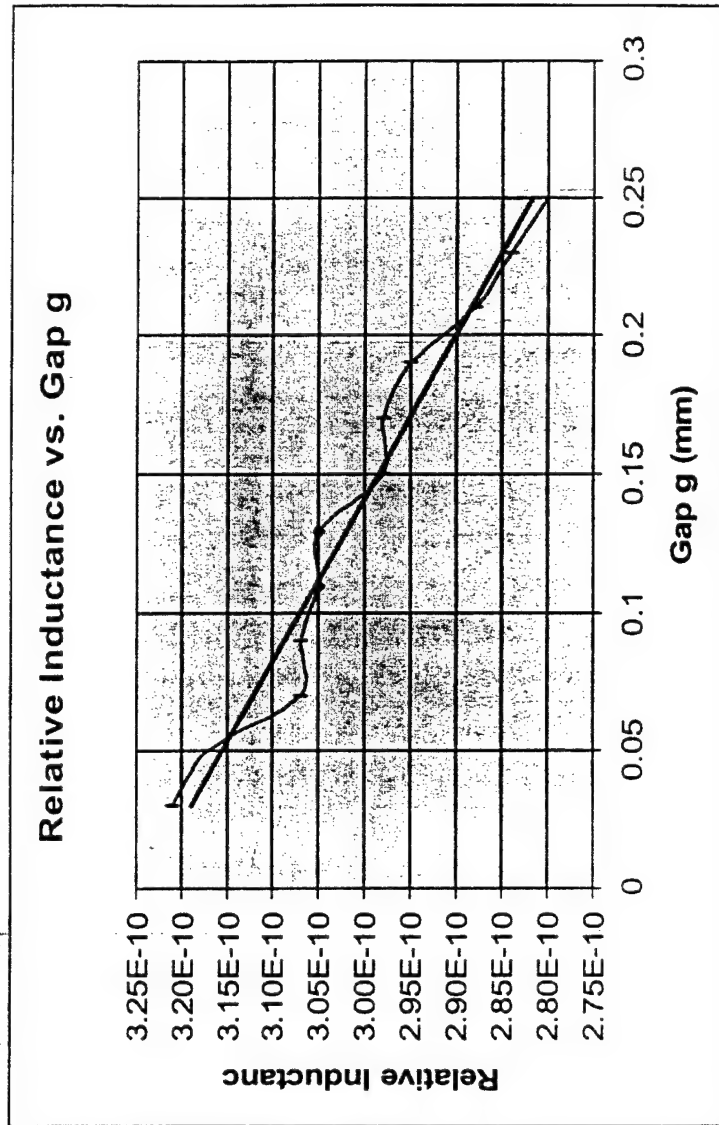


Figure 2.10



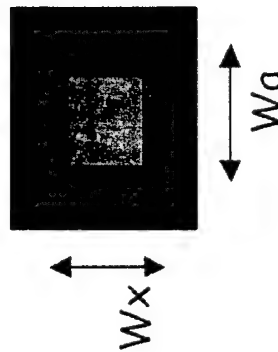
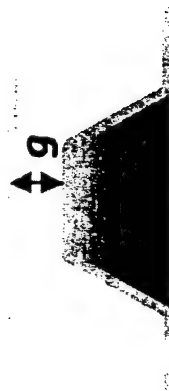
— Quasi-Static

— Full-Wave



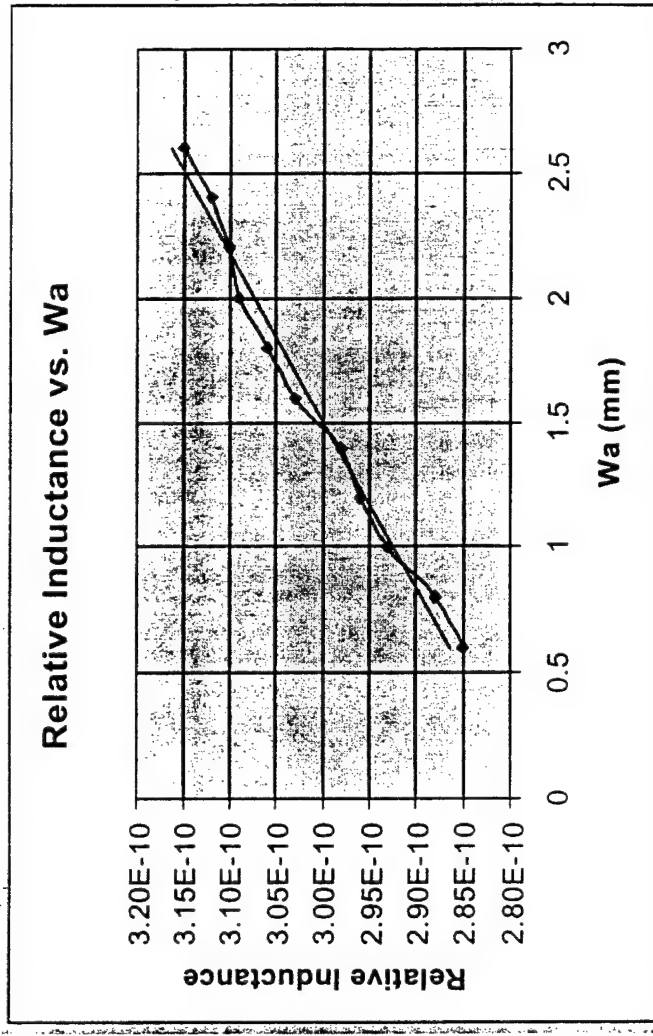


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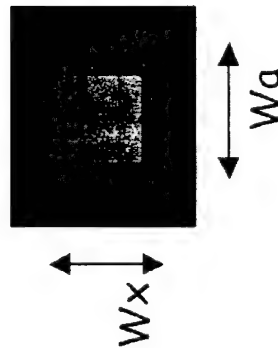
— Quasi-Static

— Full-Wave



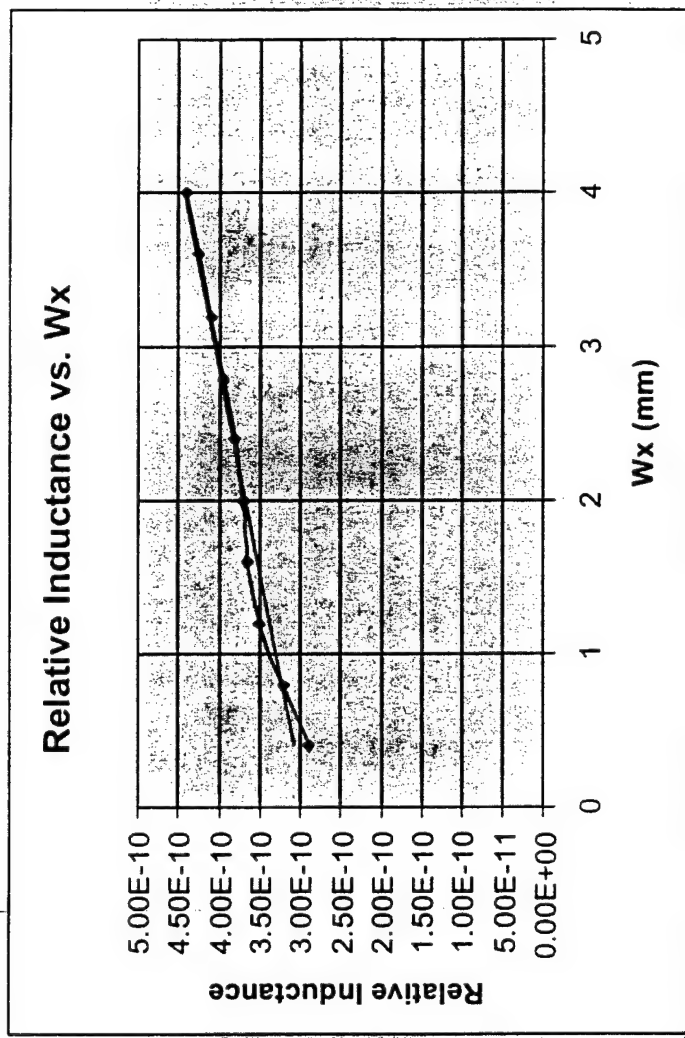


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Quasi-Static

Full-Wave

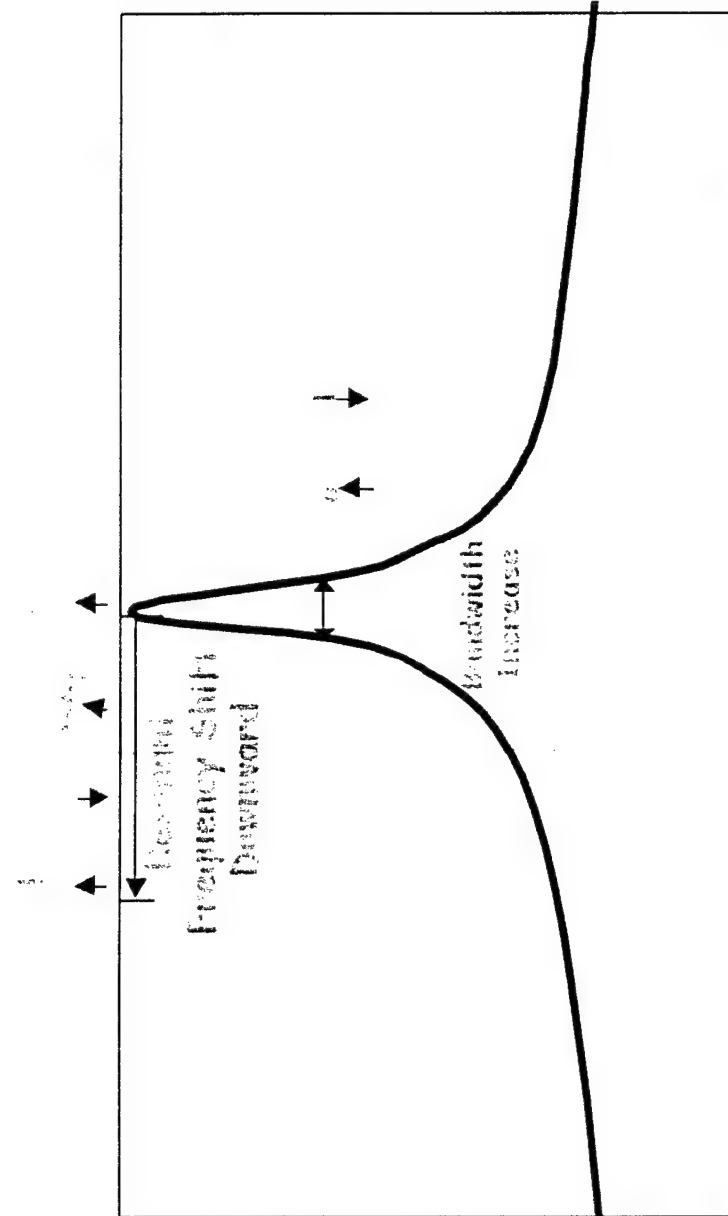




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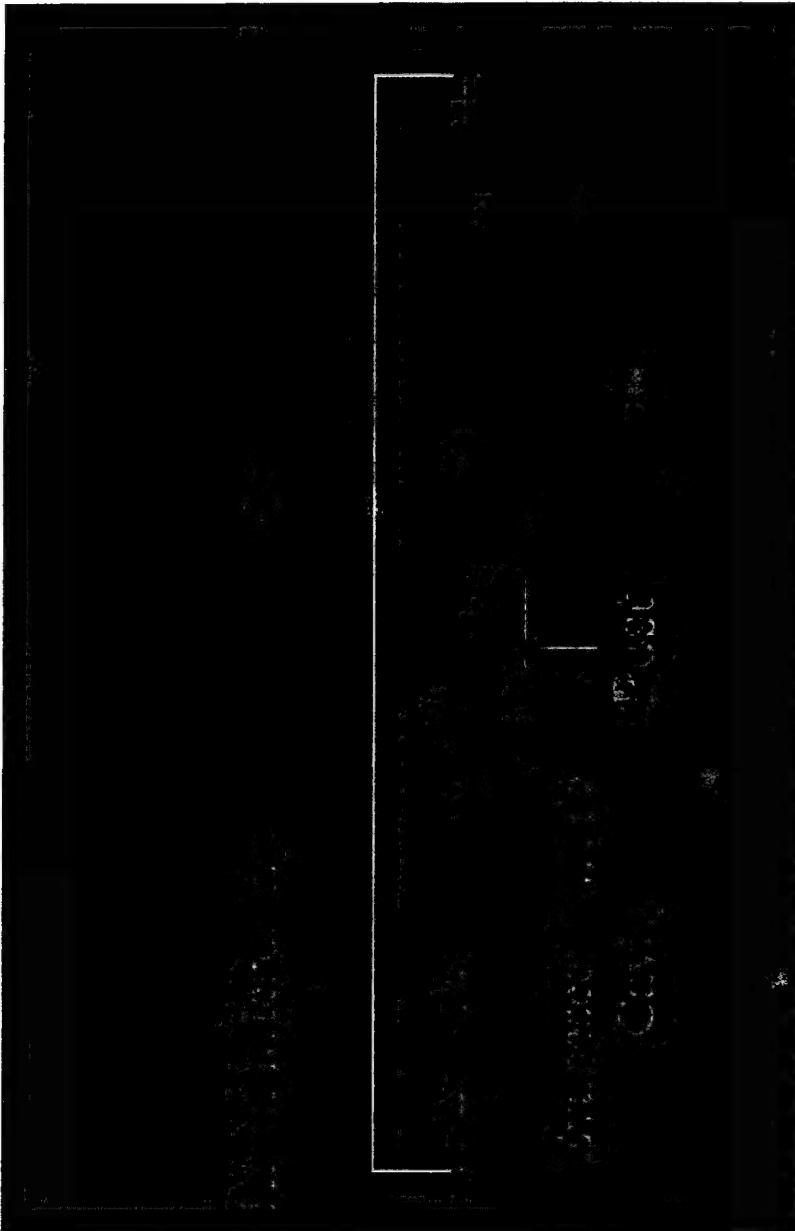
521

Frequency



University of Michigan Library

Ann Arbor, Michigan

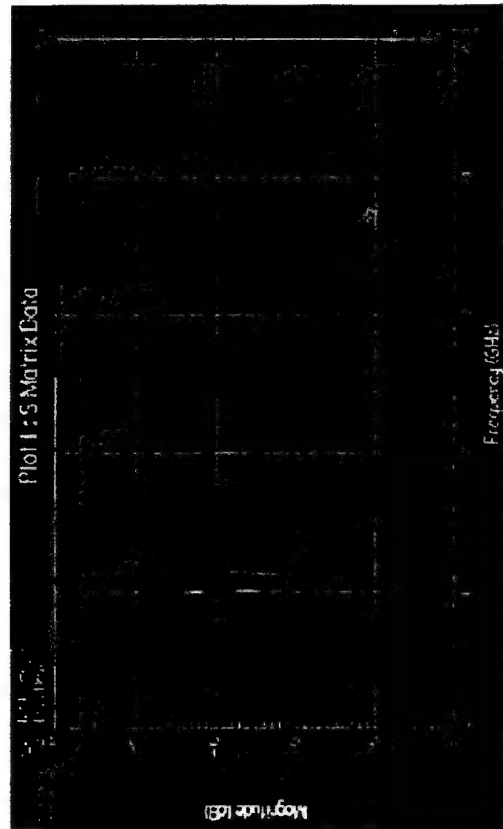




10 mm x 7 mm x 0.5 mm
With a Q of 500

10 mm x 7 mm x 0.5 mm
With a Q of 500

S-Parameters

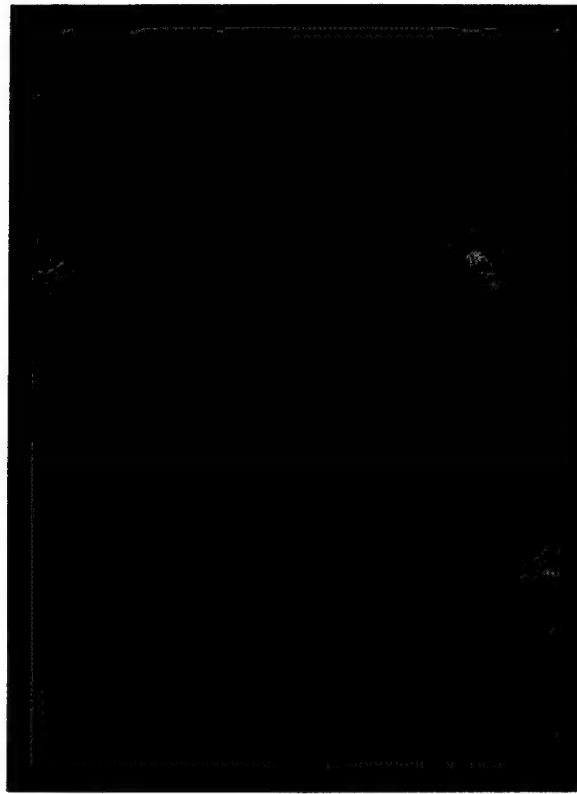
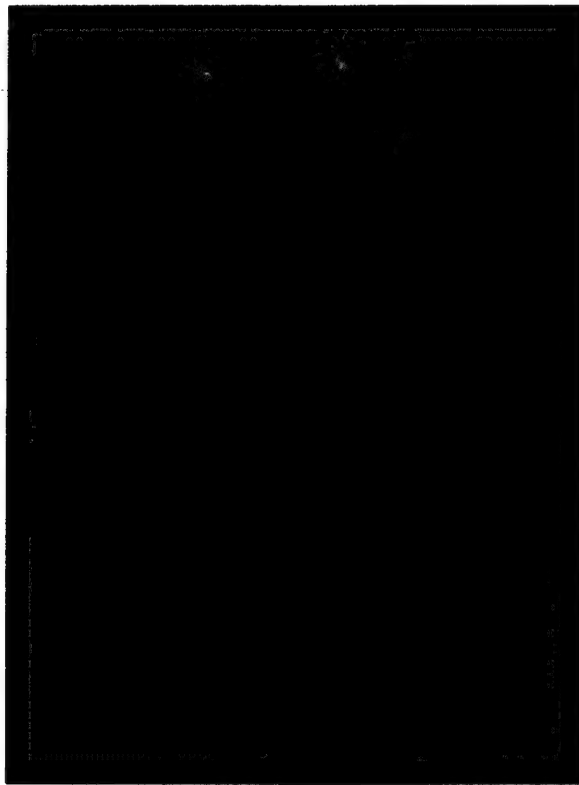




Single Pole Evaluation and Model

© 2012

Microwave Model



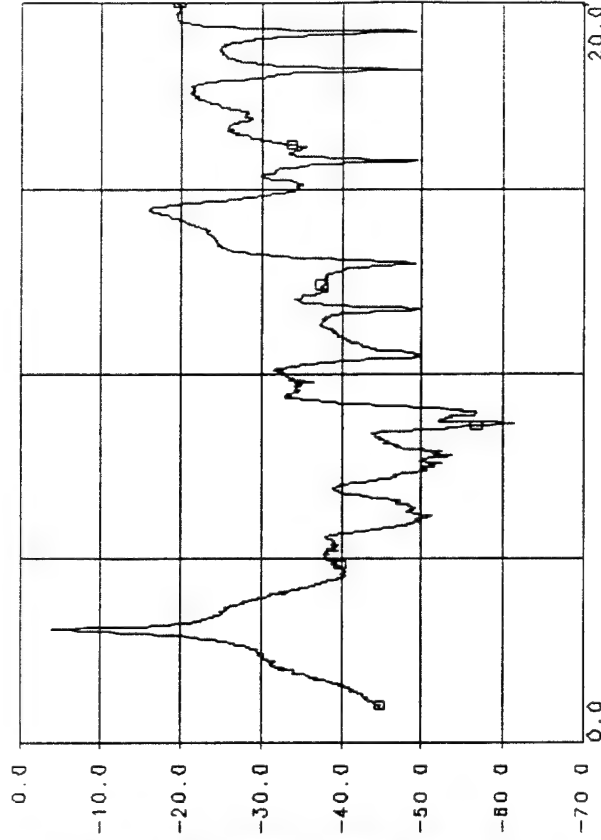
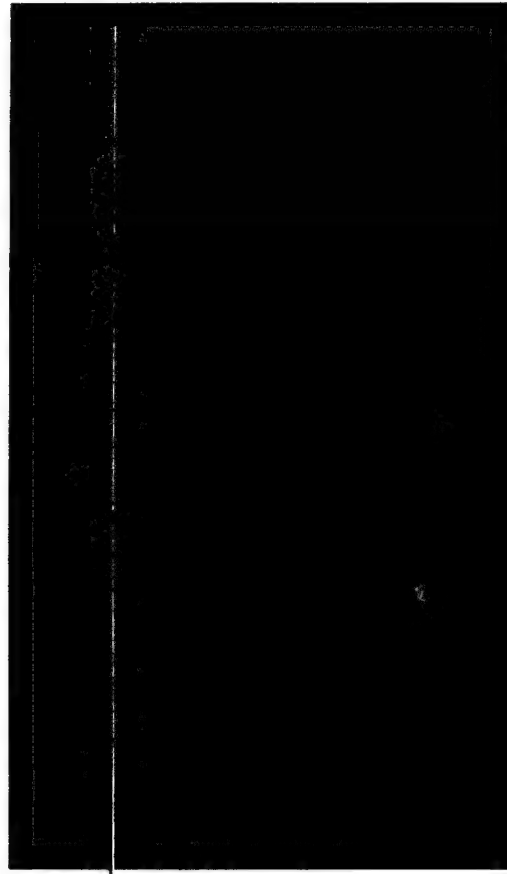
20mm x 17mm x 2mm



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Single Pole Excitation and Modes

Chen, 1997, 1998



Microwave Model



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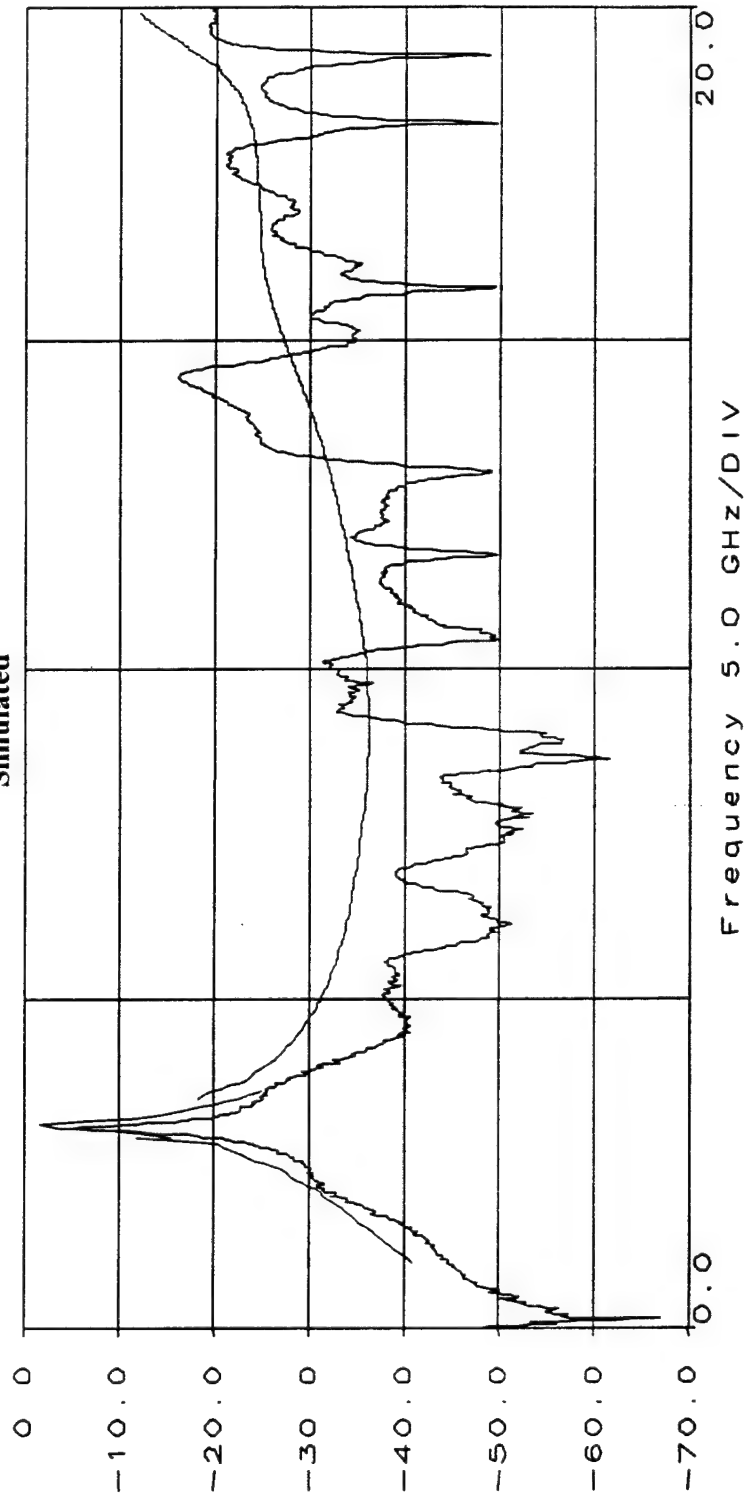
Electronics Technology Department

Department of Electronics Technology

Department of Electronics Technology

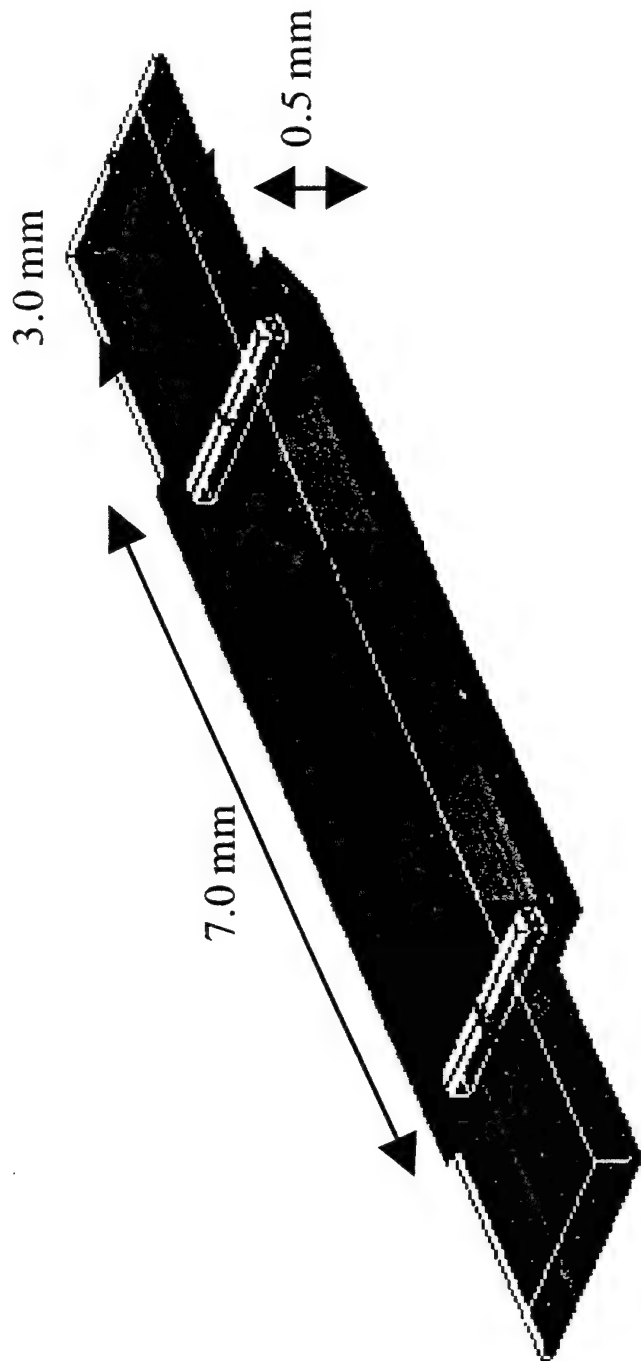
Measured

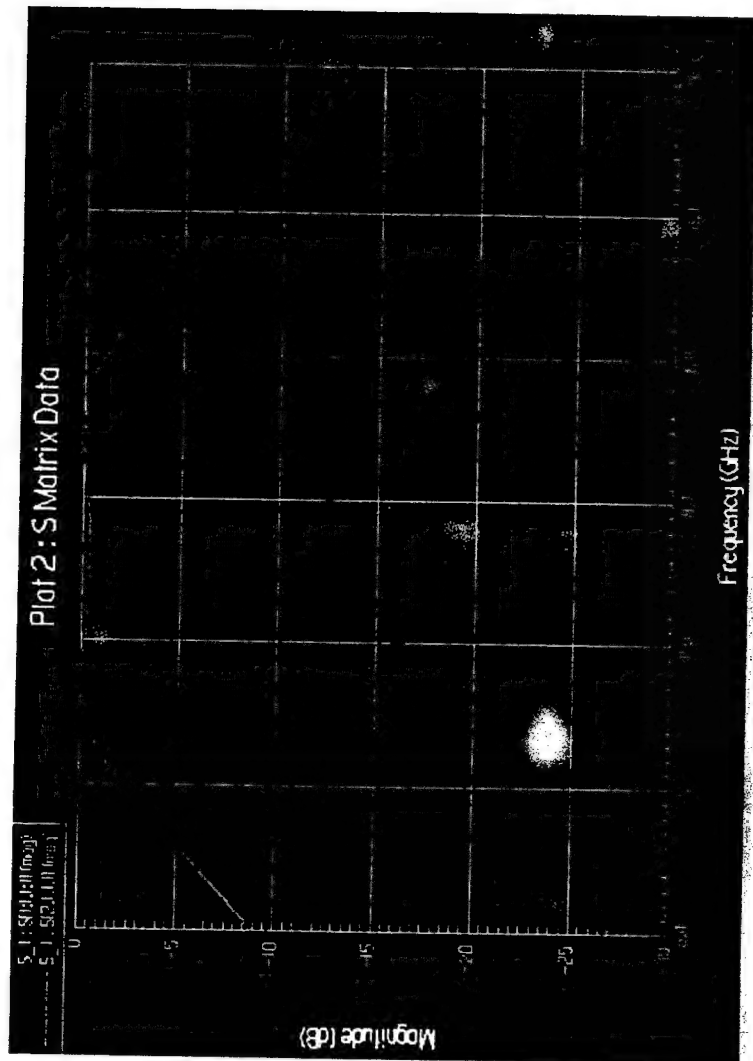
Simulated





Journal of Polymer Science: Part A: Polymer Chemistry

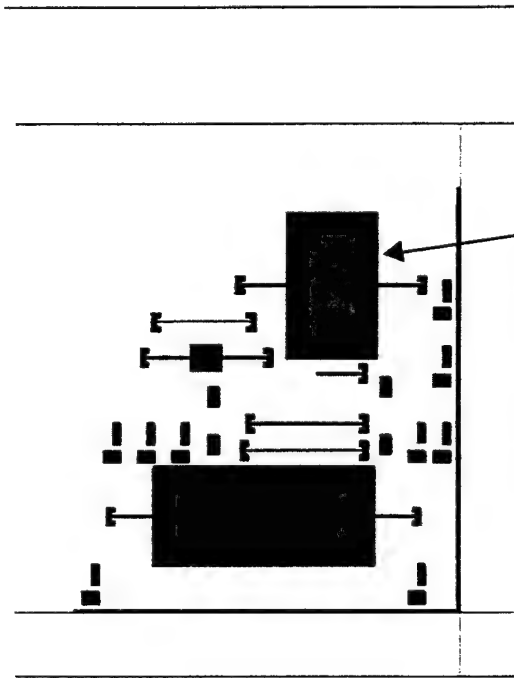






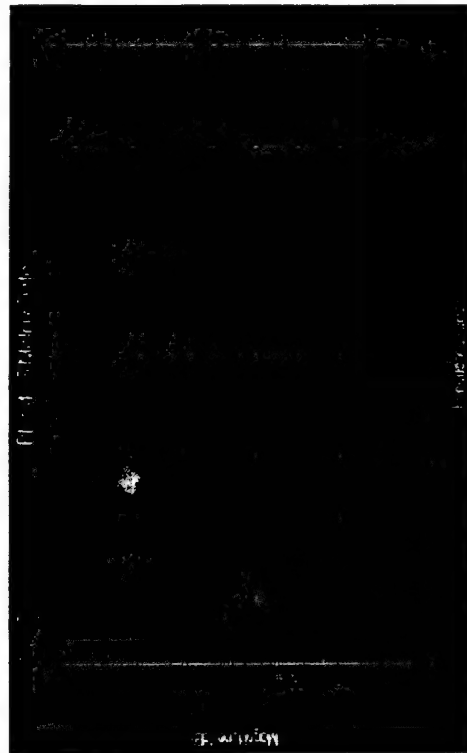
Michigan Advanced Research Laboratory

Department of Electrical Engineering



Resonator on Mask

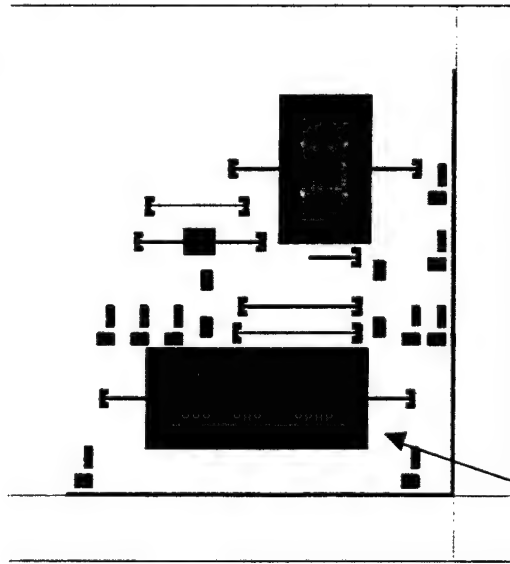
Expected $Q =$



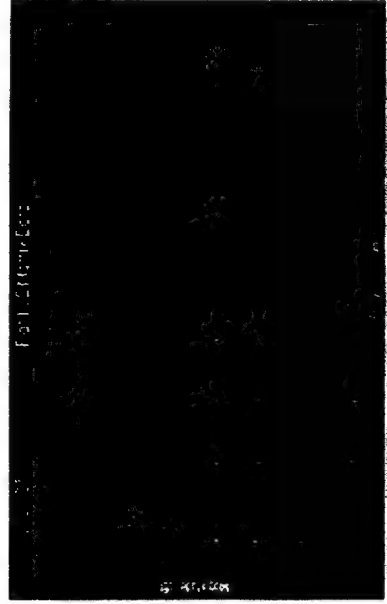
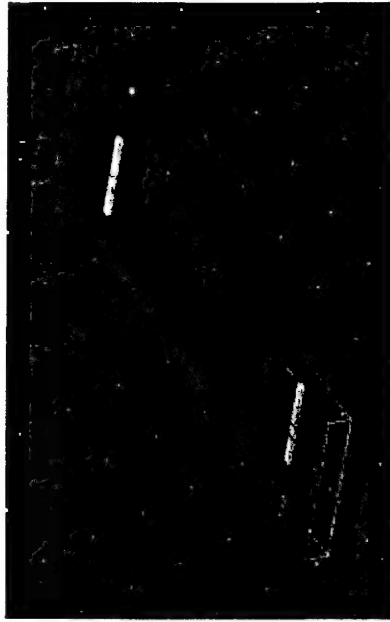


Sealed and sealed properly in a vacuum bag

Sealed and sealed properly in a vacuum bag

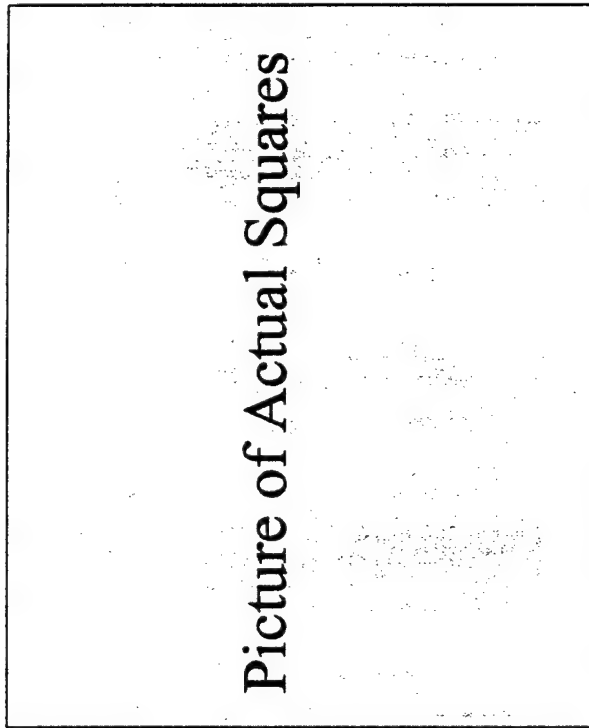


Bandpass Filter on Mask

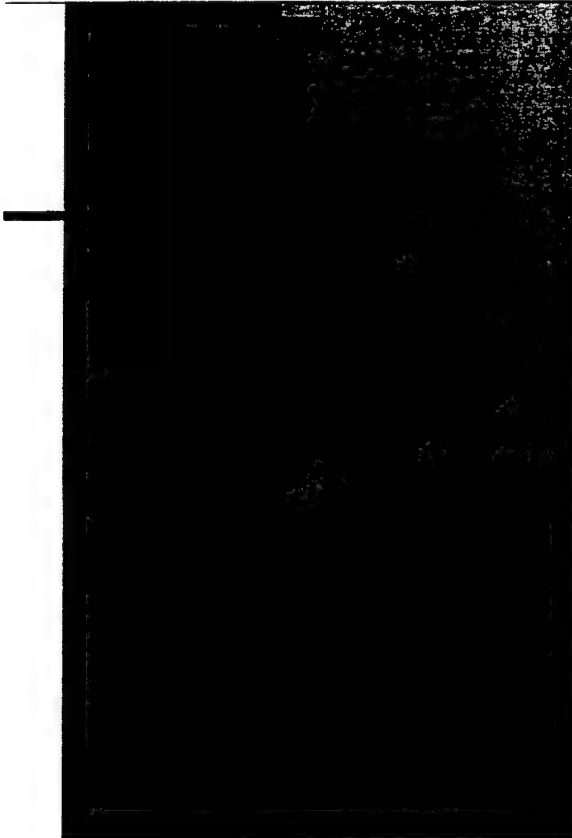




Compensation Square



Picture of Actual Squares



Compensation Square

Rule is $a=1.5 \times \text{Etch Depth}$



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MEMPHIS

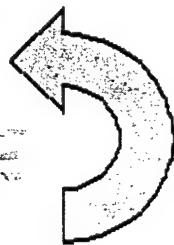
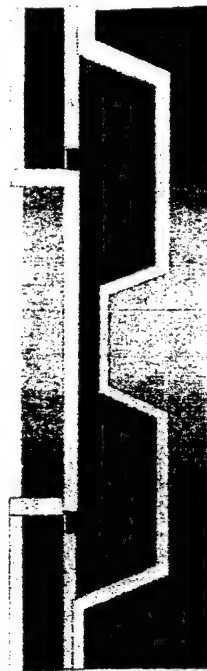
Upper Wafer



Lower Wafer



Assembled Resonator





Microstrip and Grounding Via

Microstrip



Coupling Slot



Pattern SiO₂ &
Sputter Ti/Au/Ti

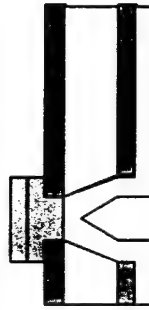
Pattern Microstrip
& E-Plate



Grounding Via



Etch Vias



Sputter Ti/Au/Ti

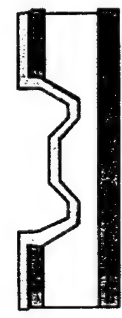
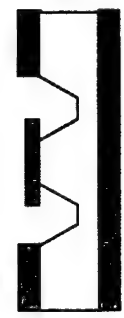


Pattern Slots and E-Plate



Thin SiO₂ Pattern SiO₂ Etch Cavities Thin Post & Sputter Gold

Post

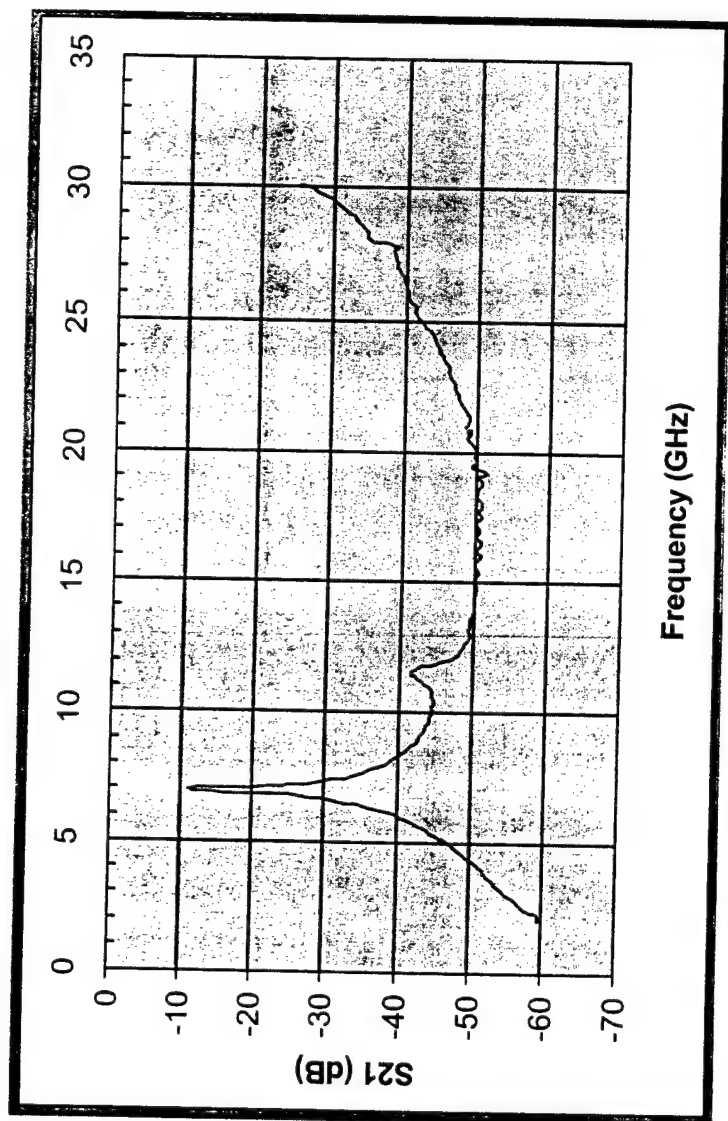




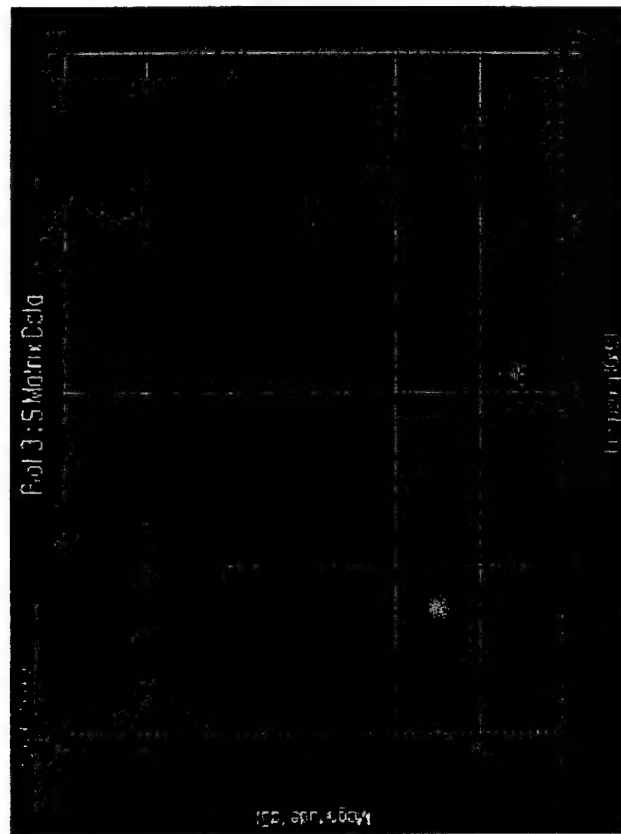
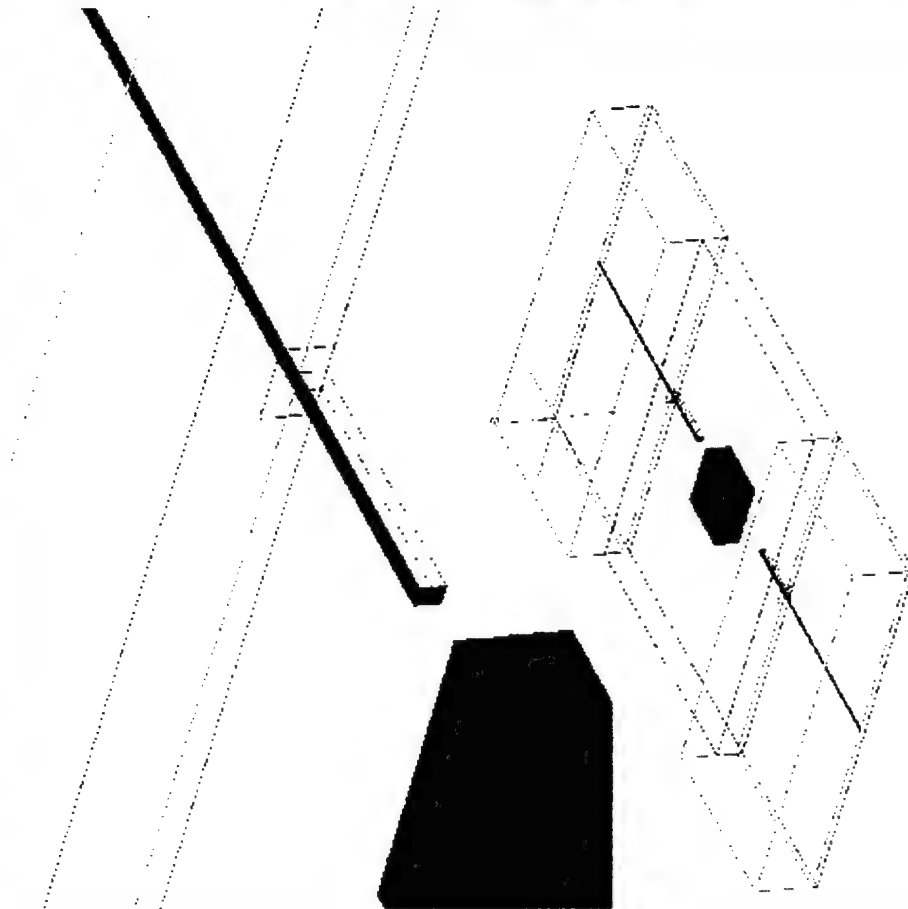
2010-2011 Academic Year

Electrical Engineering

EE-559: Microwave Engineering



Insertion Loss due to Lines (0.1 dB), $Q_L=90$, $Q_U=130$



Size: 6 by 4.5 by 1.5

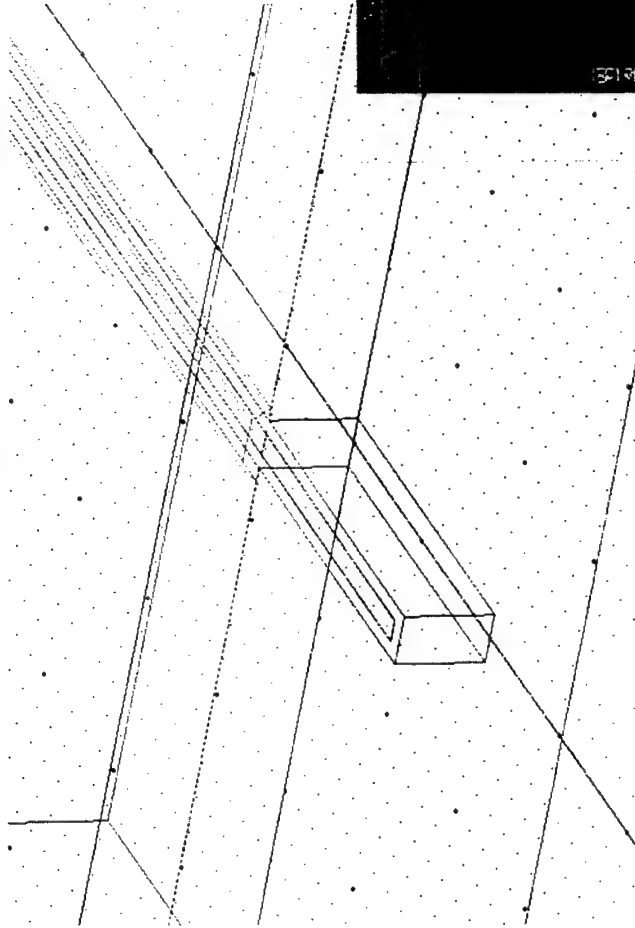


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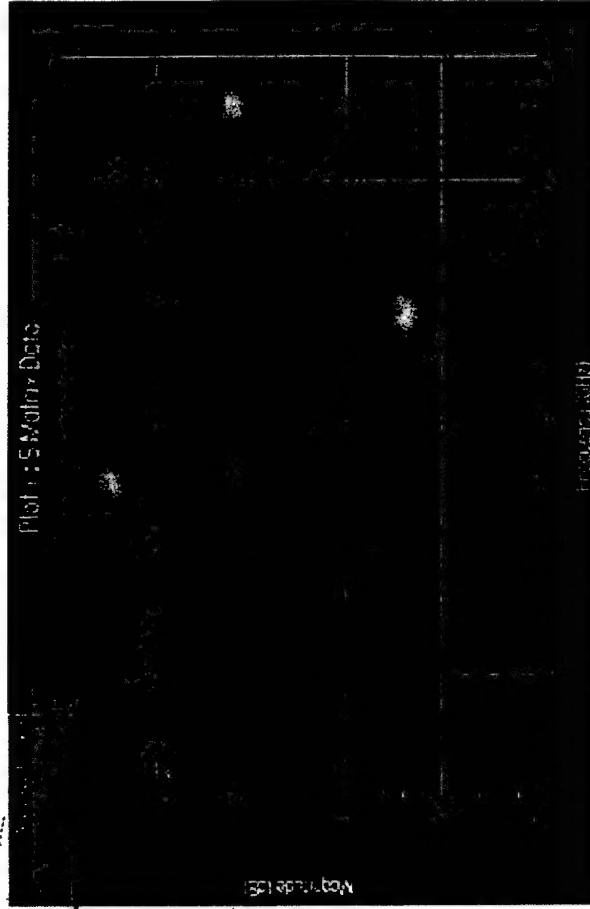


U of M



U of M

U of M



Size 20 by 10 by 10



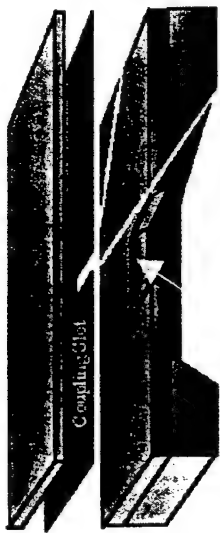
- Tuning External Coupling
- Tuning Post Capacitance
- Tuning of Internal Coupling or
Evanescent Mode Sections



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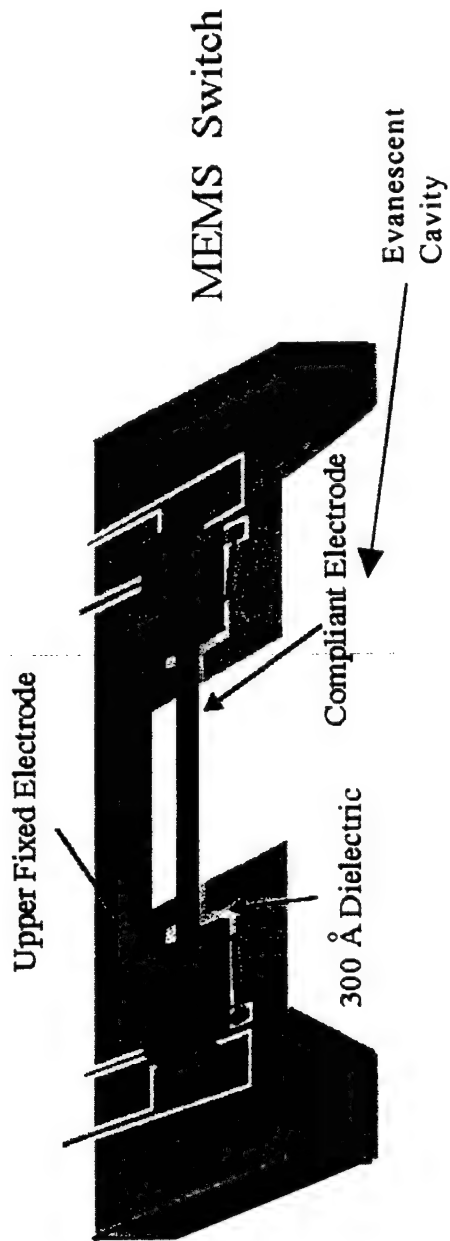
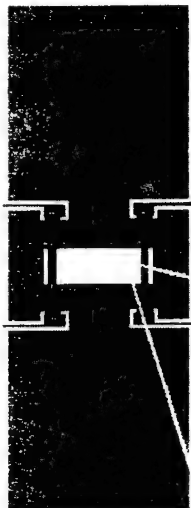


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Department of Electrical Engineering
Ann Arbor, Michigan 48106-1364
Tel: (313) 763-3400
Fax: (313) 763-3400
E-mail: ee@umich.edu



Evanescent Mode
Cavity

Coupling Slot with Switches





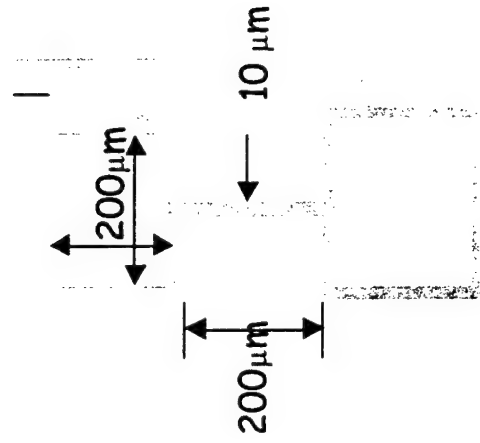
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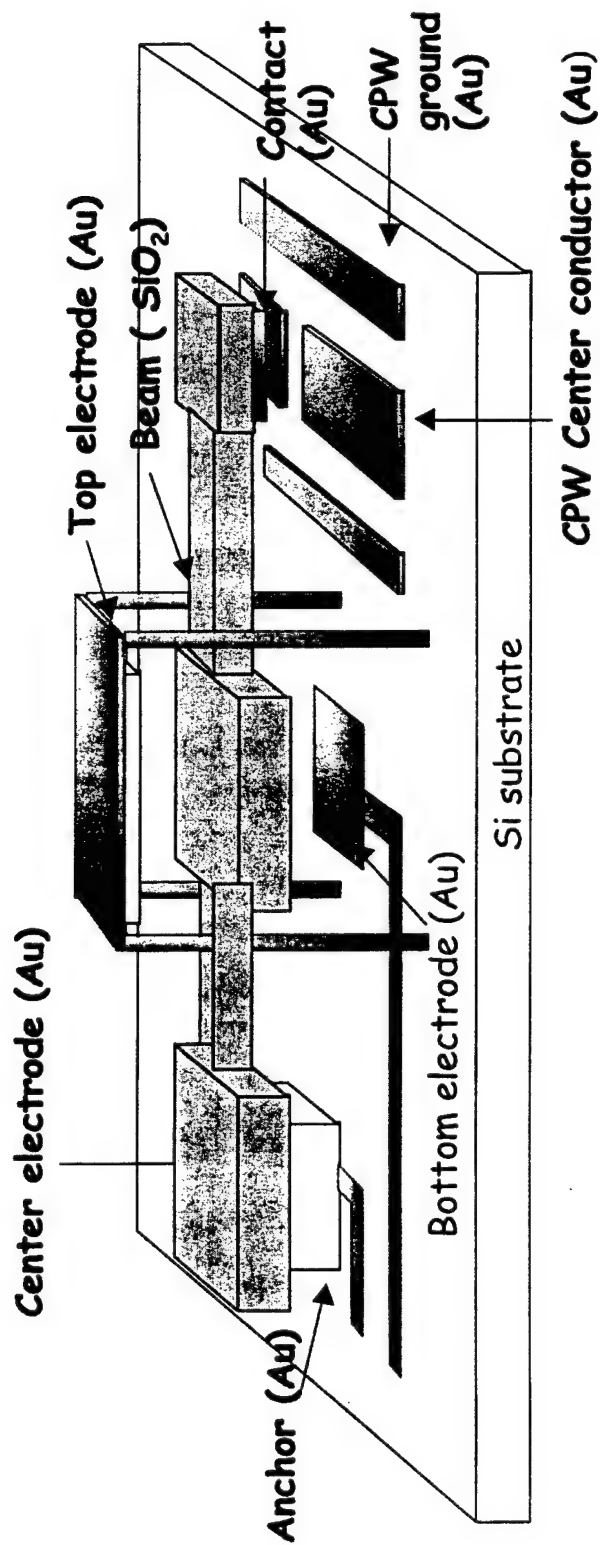
Tuning Input Shift Key and Capacitor via a Cantilever Beam

500 μm

6

$W = 60 \mu\text{m}$
 $G = 40 \mu\text{m}$
 $f = 30 \text{ GHz}$







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Library

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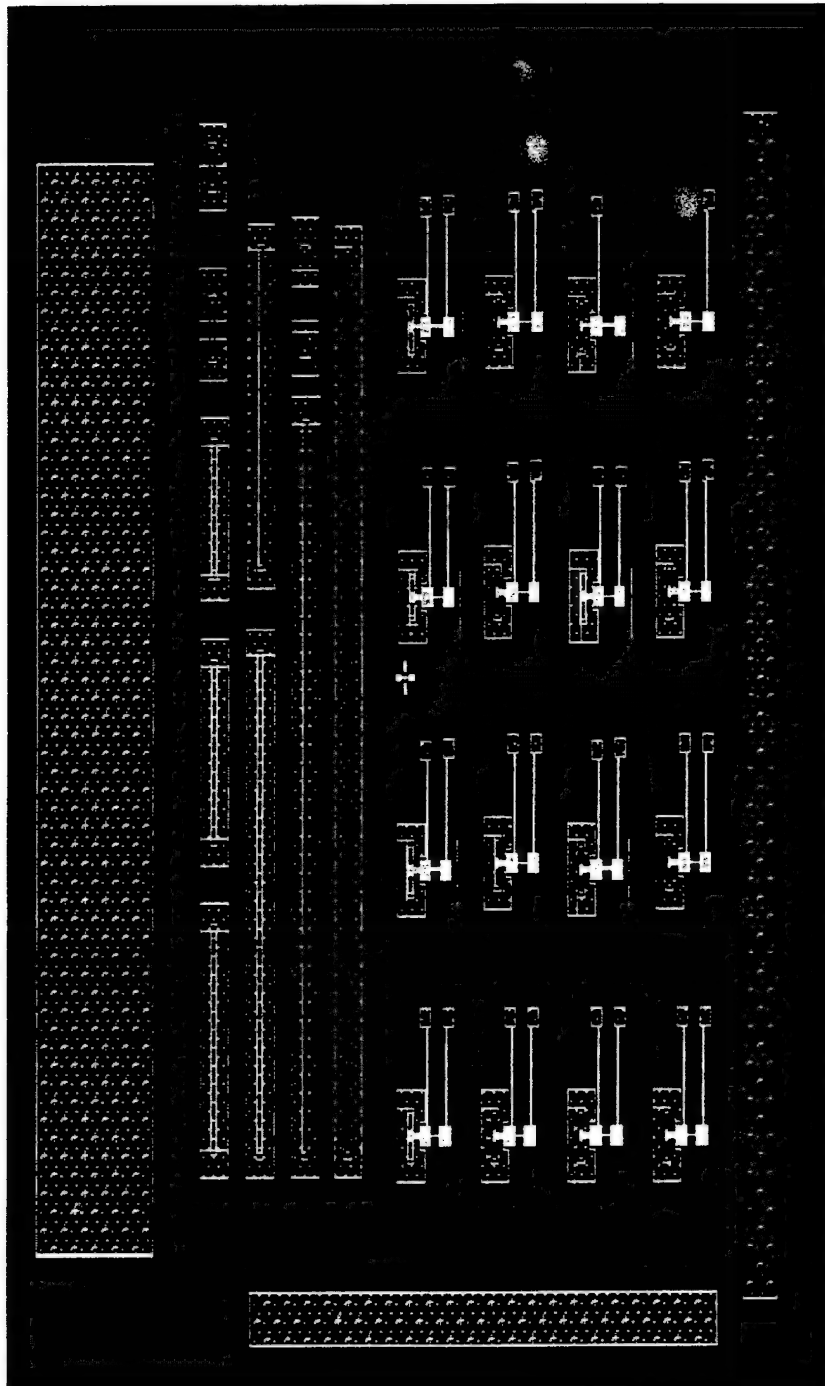
$$k = \frac{wEt^3}{4l^3}$$

Beam length (μm)	Beam width = 10(μm)	Beam width = 20(μm)
180	0.24 N/m	0.48 N/m
200	0.18 N/m	0.35 N/m
220	0.13 N/m	0.26 N/m
240	0.10 N/m	0.20 N/m

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$$V_{pi} = \sqrt{\frac{8kd^3}{27\epsilon_0 A}}$$

Beam length (μm)	Beam width = 10(μm)	Beam width = 20(μm)
180	3.59 V	5.07 V
200	3.06 V	4.33 V
220	2.64 V	3.73 V
240	2.31 V	2.27 V





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Adjunct Faculty

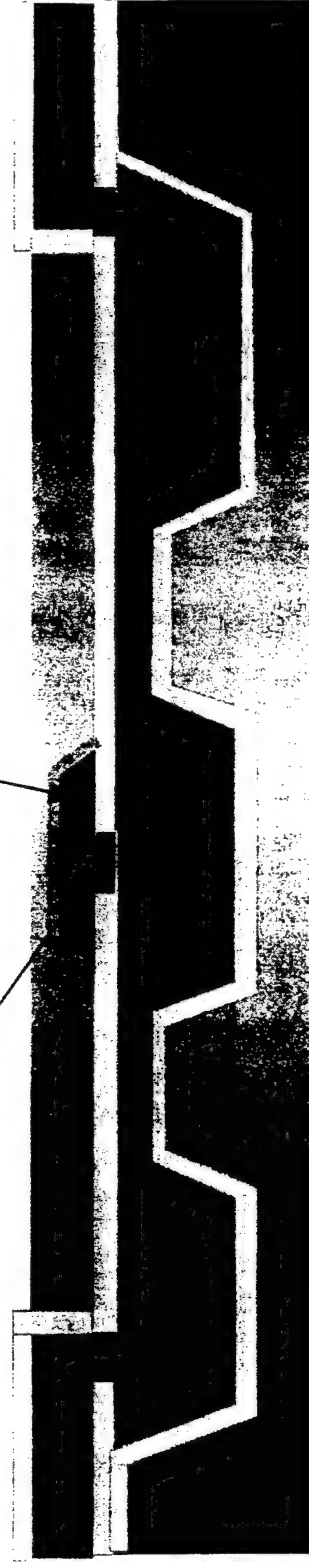
Electrical Engineering

Microstrip Line

Auxiliary Evanescent

Coupling Slot

Cavity

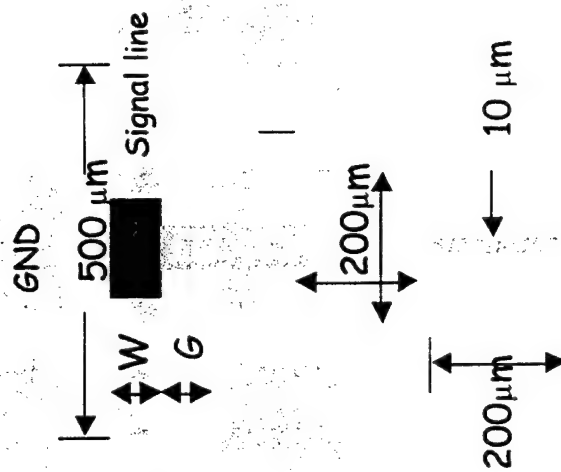




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$W = 60 \mu\text{m}$
 $G = 40 \mu\text{m}$
 $f = 30 \text{ GHz}$





•Tip deflection

$$d = \frac{kL^2}{2}$$

$$k = \frac{1}{r} = \frac{6b_1b_2E_1E_2t_1t_2(t_1+t_2)(a_2-a_1)\Delta T}{(b_1E_1t_1^2)^2 + (b_2E_2t_2^2)^2 + 2b_1b_2E_1E_2t_1t_2(2t_1^2 + 3t_1t_2 + 2t_2^2)}$$

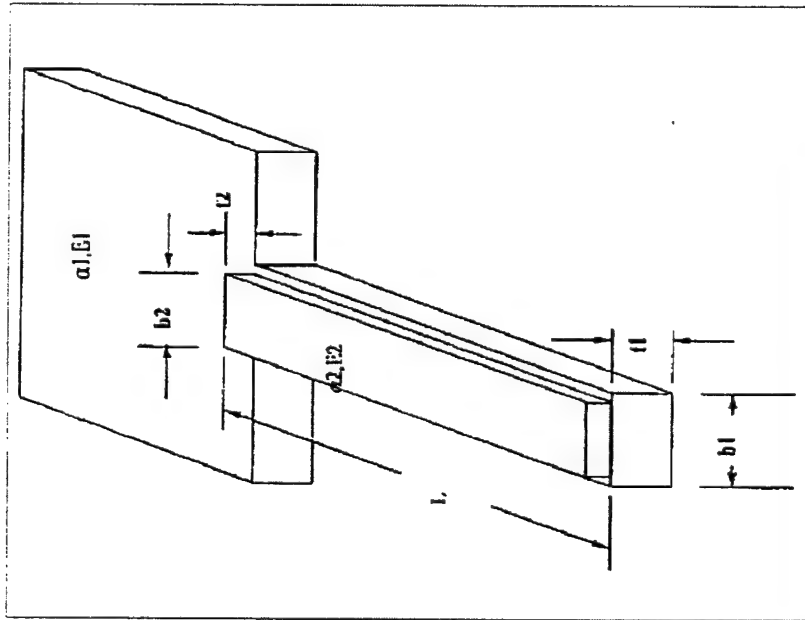
•Max force at the tip of cantilever

$$F = \frac{3Eld}{L^3}$$

•Conversion factors :

$$\gamma = \frac{d}{\Delta T} \quad (\text{e.g. } 0.1 \text{ } \mu\text{m/K})$$

$$\gamma_E = \frac{d}{P_{IN}} \quad (\text{Typically } 0.5\text{-}1 \text{ } \mu\text{m/mW})$$



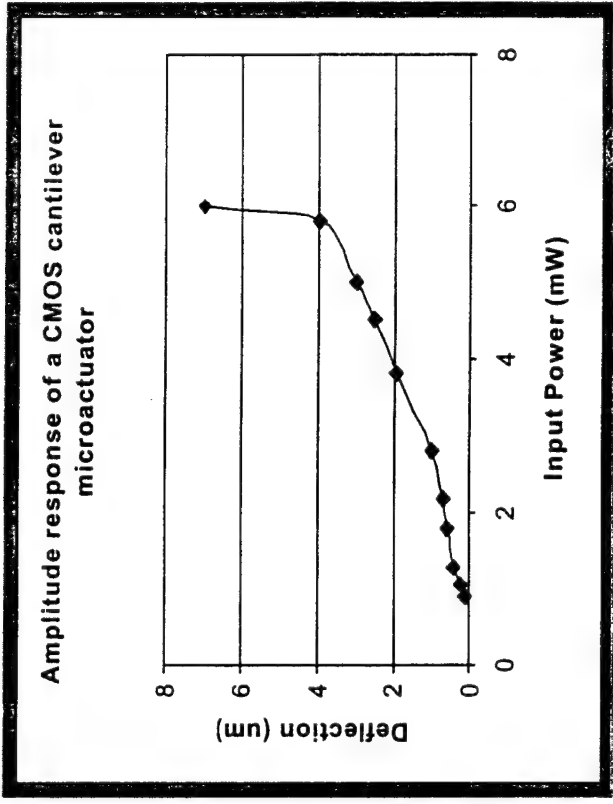
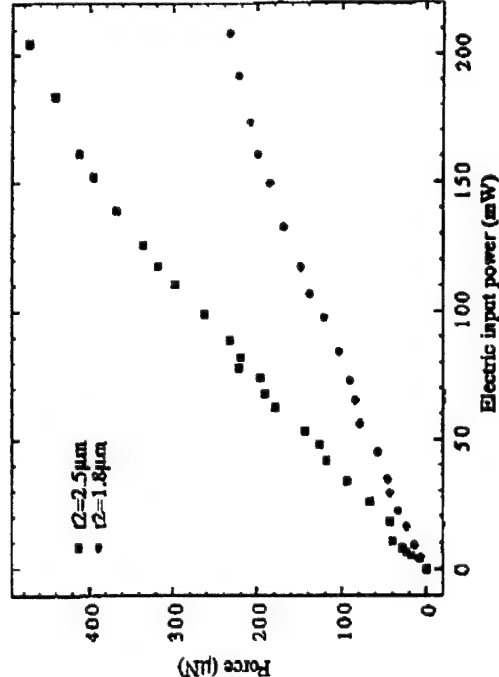
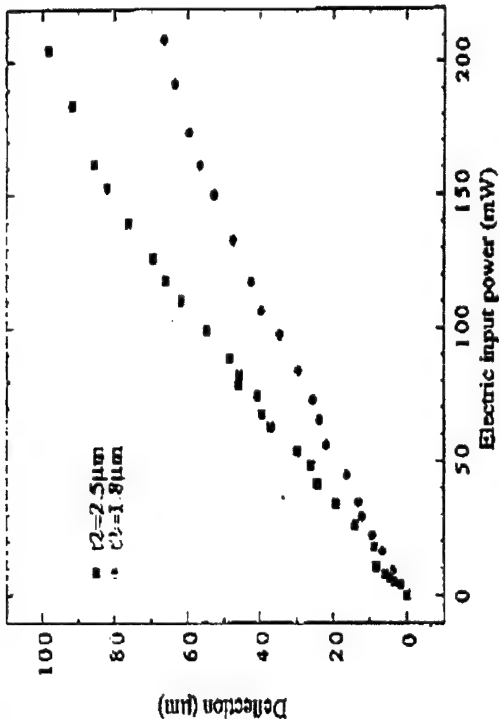


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Amplitude response of a CMOS cantilever microactuator

$L \approx 170 \mu\text{m}$ $w \approx 10 \mu\text{m}$

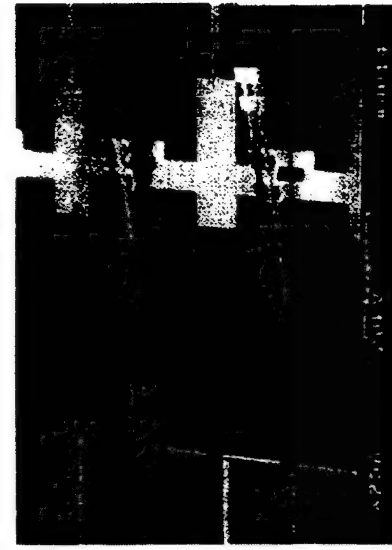
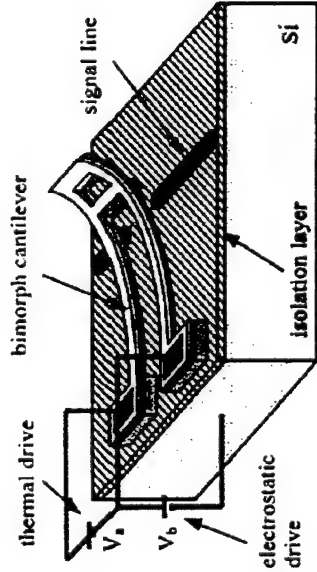




MEMORANDUM

TO : Mr. J. R. Rabinovich

FROM : Mr. J. R. Rabinovich



Basic Characteristics

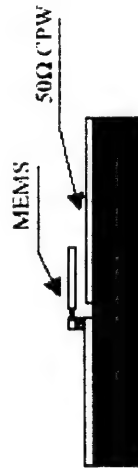
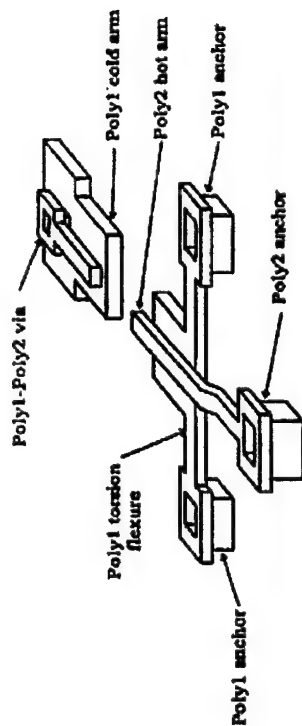
- Gold-to-gold electrical contact
- $R_{ON} = 0.6 - 0.8 \Omega$
- $L = 328 \mu m, w = 16 \mu m$
- Combined thermal and electrostatic actuation
- Beam : $TaSi_2/SiO_2$
- Beam deflection : not reported
- Power (min) : $8.0 mW / 20 V$
- Life time :
 - a) 10^6 cycles (hot switching @ $2 mA/2KHz$),
 - b) only several cycles (sticking) @ $5 mA/low$ frequency



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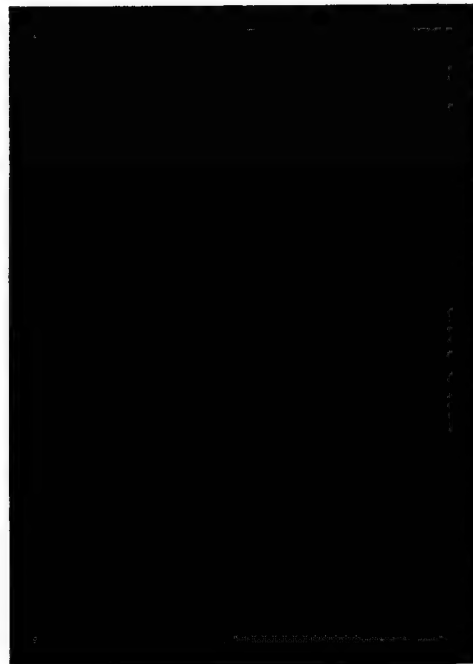
Case 2 : Cantilever beam with variable capacitance

K.C. Gupta et. al., AIT (2000)



Basic Characteristics

- Variable capacitor (5:1)
- No metal-to-metal contact
- $L = 300 \mu\text{m}$, $w = 200 \mu\text{m}$
- thermal actuation
- Beam : poly-1/poly-2
- Beam deflection : $1.6 \mu\text{m}$
- Power : not reported (volt : 5V)
- Life time : not reported





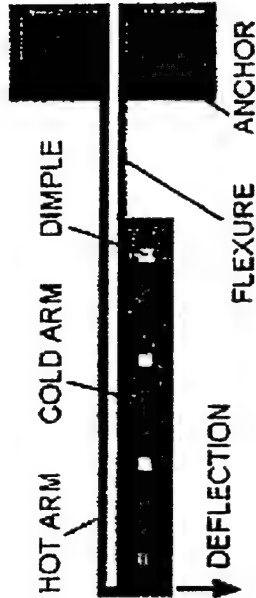
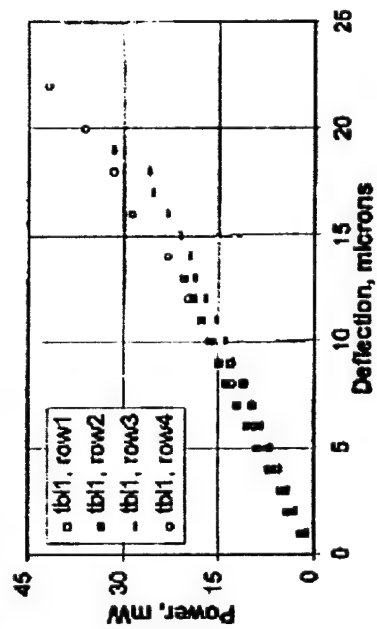
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Case 3 : Lateral cantilevered beam actuators A 1999

Table 1
Best dimensions for unloaded or lightly loaded actuators

Length	Hot arm width	Flexure length	Gap width	Max. deflection	Max. pow (mW)
150	1	30	1.5	9	13
200	1.5	50	1.5	13	21
250	1.5	80	1.5	19	32
300	2	75	1.5	22	42

All linear dimensions are in μm .



Basic Characteristics

- $L = 220 \mu\text{m}$, $w_{\text{hot arm}} = 2.5 \mu\text{m}$
- thermal actuation
- Beam : poly hot/poly cold
- Beam deflection : $\sim 15 \mu\text{m}$
- Power : 15 mW
- Life time : $980 \cdot 10^6$ cycles @2KHz



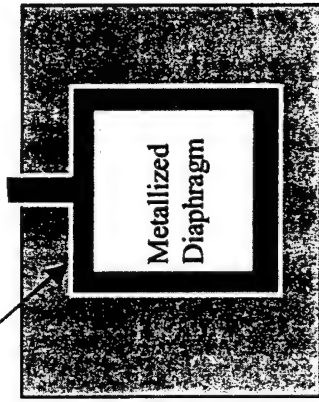
Diaphragm in Up Position



Diaphragm on Up Position



Slit Separating DC Electrodes



Metallized Cavity Walls

Diaphragm in On (DOWN) position



Diaphragm on Down Position



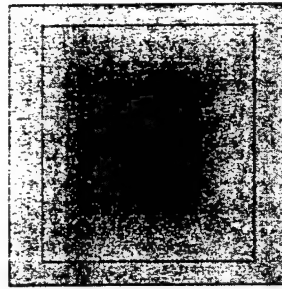
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Types of Diaphragms

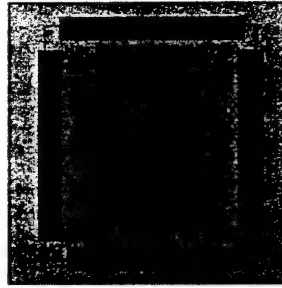
Fixed Electrode

Membrane

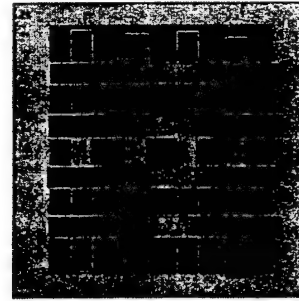
Capacitive Post



(a)



(b)



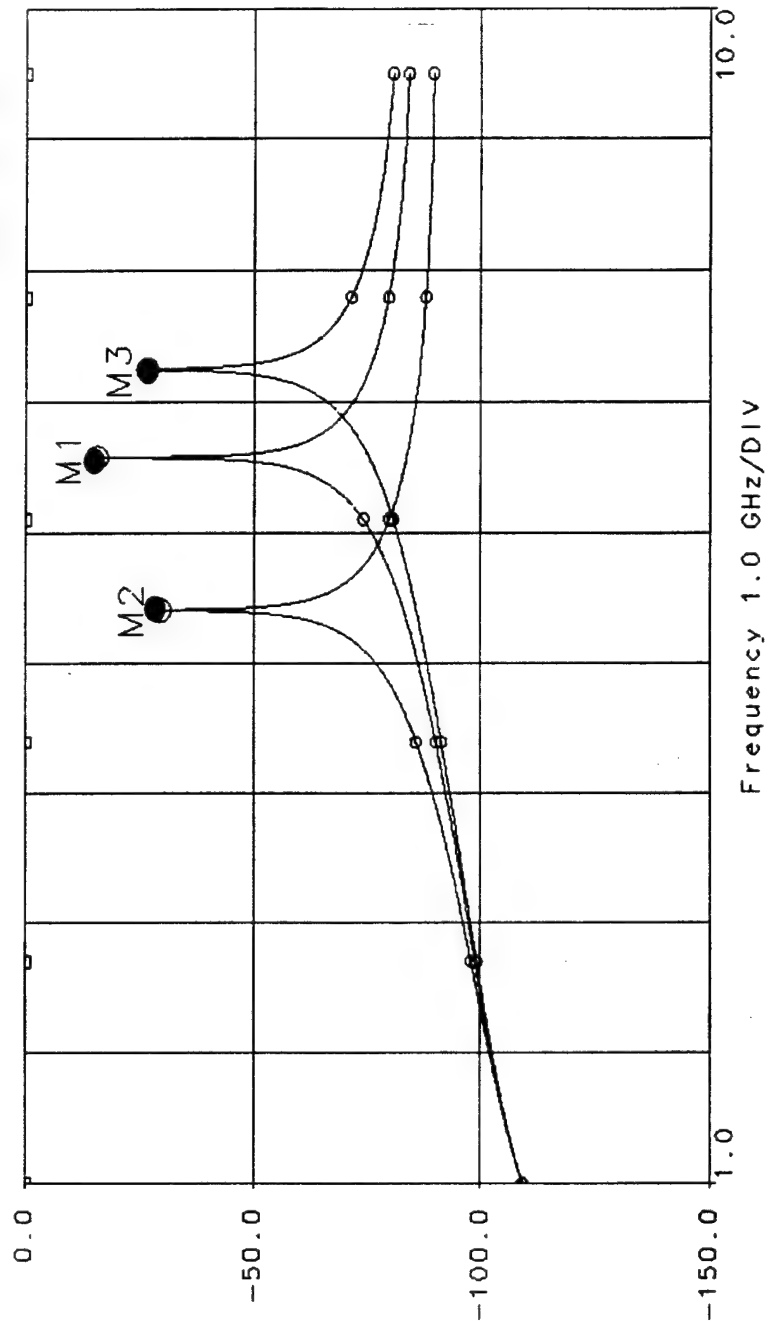
(c)



Post Gap $g=10\text{ }\mu\text{m}$
Tolerance $T=5\text{ }\mu\text{m}$

```
EMF_Test_tb
SMAT1
EMR_w_Slots
S[1,1]
dB
```

○EMF_Test_lb
SMAT1
EMR_w_Slots
S[2..1]
dB





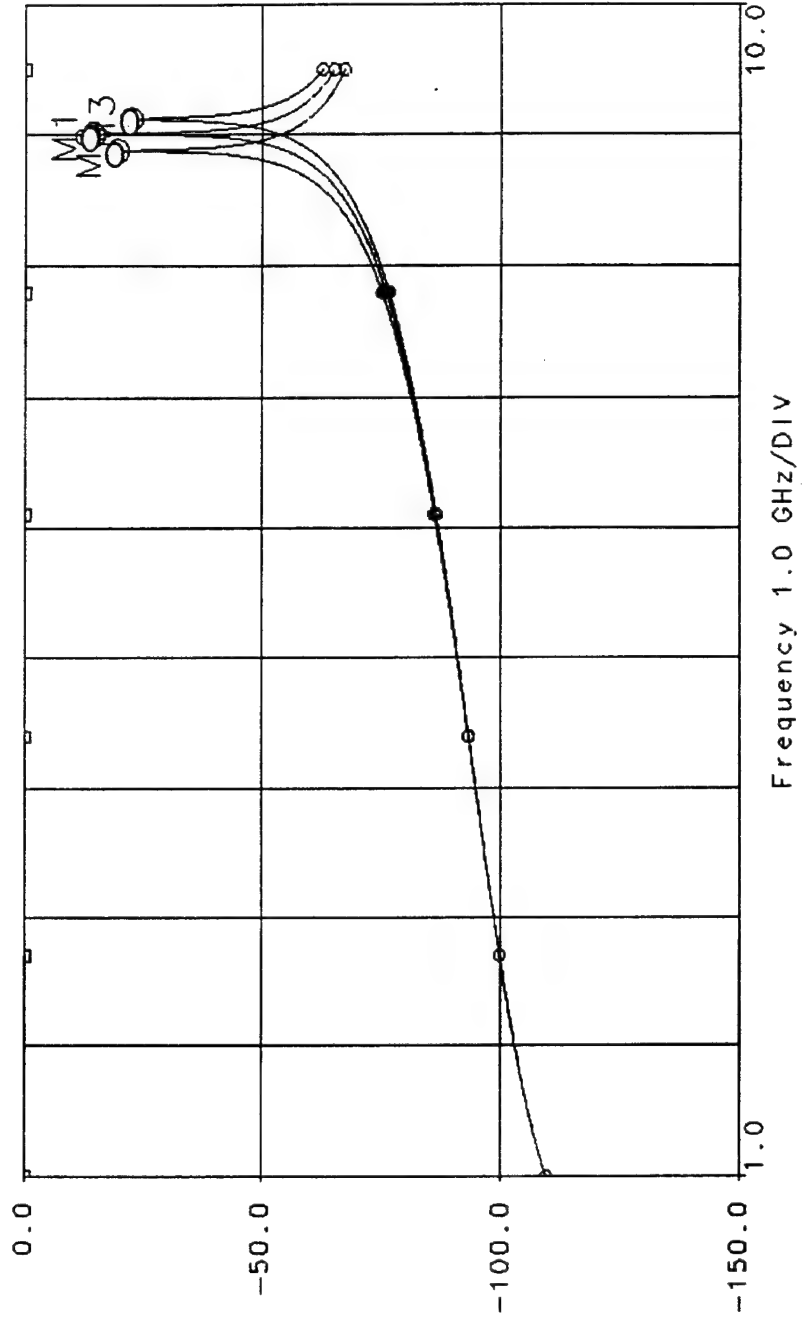
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Fabrication Tolerance vs. Tuning

□ EMF_Test_tb
SMAT1
EMR-w_Slots
S[1,1]
dB

○ EMF_Test_tb
SMAT1
EMR-w_Slots
S[2,1]
dB

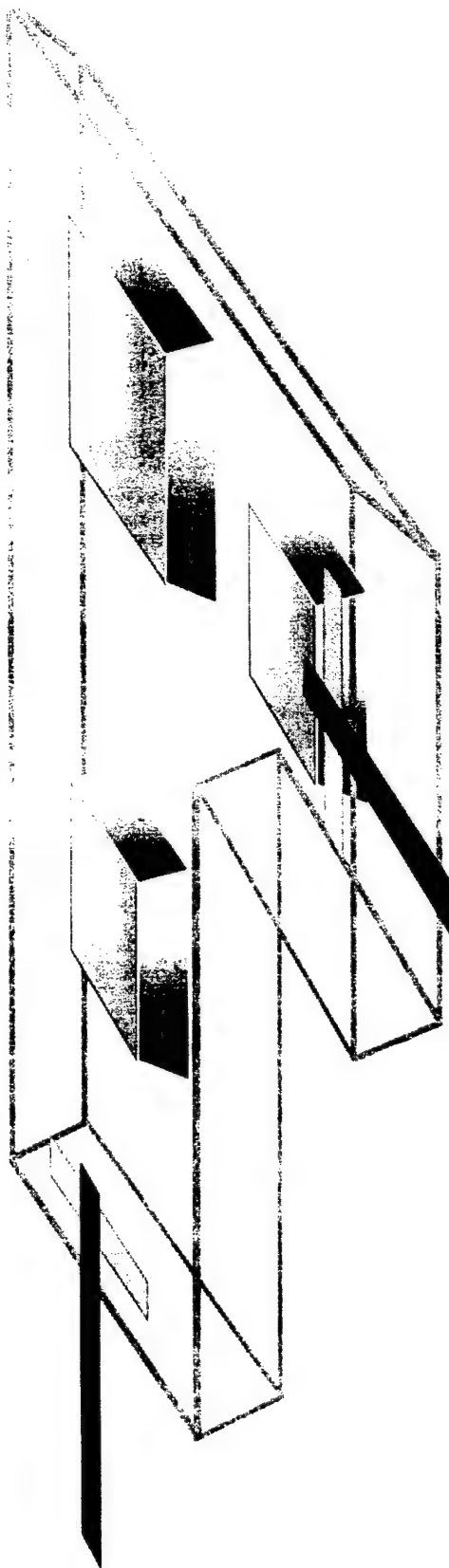
Post Gap 1.50 mm
Tolerance 1.00 mm





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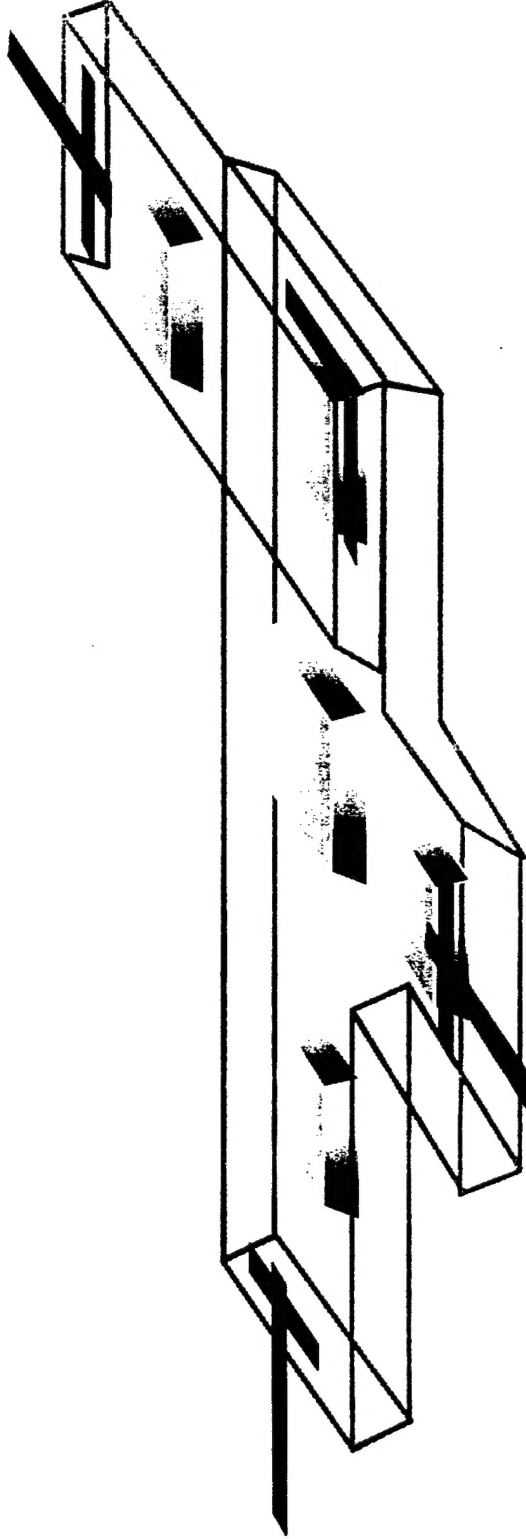
Three Fold Letter





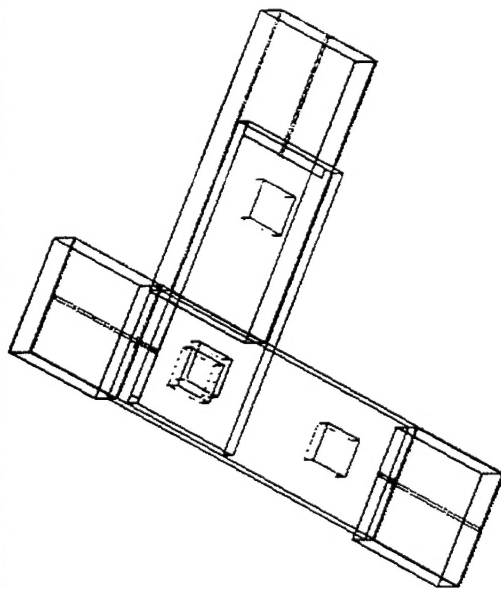
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Diploma

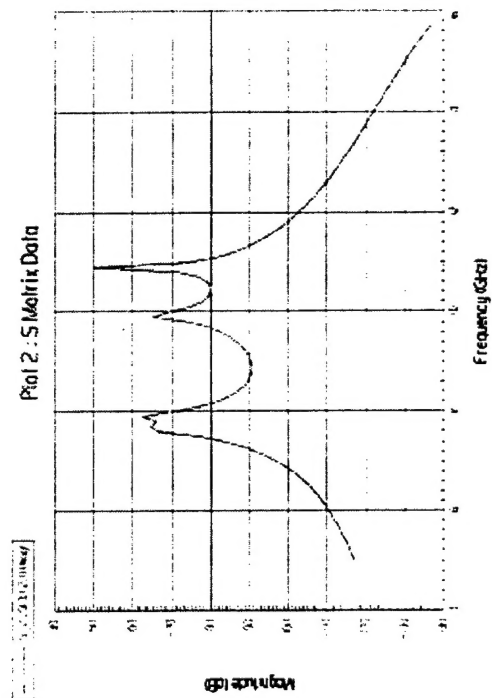
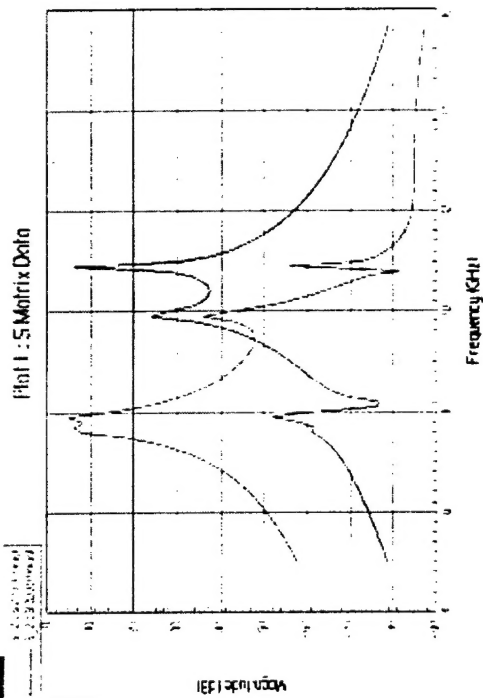




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Diplexer Possibility





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Accomplishments in Microwave Engineering Evanescent Mode Filters

Development of a Design Process for multi-pole filters
Theoretical Simulation of a multipole filter using full-wave techniques to
verify method
Successful comparison to LIBRA
Demonstration of a single pole evanescent mode filter with 3% bandwidth
and Q approximately 400

March 05 - March 11

March 2001							April 2001						
S	M	T	W	T	F	S	S	M	T	W	T	F	S
				1	2	3	1	2	3	4	5	6	7
4	5	6	7	8	9	10	8	9	10	11	12	13	14
11	12	13	14	15	16	17	15	16	17	18	19	20	21
18	19	20	21	22	23	24	22	23	24	25	26	27	28
25	26	27	28	29	30	31	29	30					

Monday, March 05		Thursday, March 08	
Linda -Dani needs website (Japan) Linda out of the office Japan		Steve - out of the office	
1:30pm 3:00pm Dean's Cabinet (2267 LEC)		8:30am 9:30am Judith Pitney - standing	
3:00pm 4:00pm Assoc. Deans w/Steve (2267 LEC)		9:30am 11:30am Faculty Candidates	
4:00pm 5:00pm Saeed		11:00am 11:30am Multi.Dept Collobration in High Performance Sci Computing (2210 LEC)	
5:30pm 6:30pm Kamal,Dimitrios,Linda		11:30am 12:00pm Ed Borbely - standing mtg	
6:30pm 7:30pm Sergio, Jongming, Clark		12:30pm 1:30pm NAME Faculty Mtg (232 NAME)	
		2:30pm 3:00pm Paul - standing	
		3:00pm 3:30pm Wayne - standing	
		3:30pm 4:00pm Jim Bean - standing	
		6:00pm 7:00pm Alex	
Tuesday, March 06		Friday, March 09	
Linda -Dani needs website (Japan) Linda out until 12:00noon		Steve - out of the office (until 3:30)	
9:30am 11:30am Faculty Candidates		8:30am 9:30am Faculty Candidates	
3:15pm 5:00pm CoE Chairs Mtg (2210 LEC)		10:00am 12:00pm APADG (MI League, Henderson)	
4:00pm 5:00pm kelly (EECS)		2:00pm 3:00pm John Halloran - standing (3062B HH Dow)	
5:00pm 6:00pm Jongming		4:00pm 5:00pm John W./Kyoung/Ron	
6:00pm 7:00pm Kok-Yan Lee		5:00pm 6:00pm Pallab Bhattacharya/JPL	
7:00pm 7:45pm Eray		6:00pm 7:00pm Kostas	
Wednesday, March 07		Saturday, March 10	
Linda -Dani needs website (Japan) Steve - out of the office		2:30pm 3:30pm Donghoon	
9:00am 11:00am Executive Committee (2267 LEC)			
12:00pm 7:30pm GOMAC-Linda to San Antonio for this afternoon			
4:00pm 5:00pm Parallelization			
5:00pm 6:00pm Jim, Jack, Yonkshil, Phil			
6:30pm 7:30pm Bill Chappell and Matt Little			
		Sunday, March 11	